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**BOX PATENT APPLICATION**

Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. § 1.53(b),  
is a Continuation application of PCT International Application  
No. PCT/JP00/01009 filed on February 22, 2000.

Inventor(s): SEKIGUCHI, Shunichi; OGAWA, Fuminobu;  
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KURODA, Shinichi; HASEGAWA, Yuri

For: PACKET GENERATING METHOD, VIDEO DECODING METHOD, MEDIA  
MULTIPLEXER, MEDIA DEMULTIPLEXER, MULTIMEDIA  
COMMUNICATION SYSTEM AND BIT STREAM CONVERTER

Enclosed are:

- ☒ A specification consisting of Seventy-four (74) pages
- ☒ Twenty-four (24) sheet(s) of formal drawings
- ☒ An assignment of the invention
- ☒ Certified copy of Priority Document(s)
- ☒ Executed Declaration ☒ Original ☐ Photocopy
- ☐ A statement (☐ Original ☐ Photocopy) to establish small  
entity status under 37 C.F.R. § 1.9 and 37 C.F.R. § 1.27

- ☐ Information Disclosure Statement, PTO-1449 with reference(s)
- ☐ Preliminary Amendment
- ☒ Priority of PCT International Application No. PCT/JP00/01009 filed on February 22, 2000 is claimed under 35 U.S.C. § 120.
- ☐ Amend the specification by inserting before the first line thereof the following:

--This application is a Continuation of PCT International Application No. \_\_\_\_\_ filed on \_\_\_\_\_, which designated the United States, and on which priority is claimed under 35 U.S.C. § 120, the entire contents of which are hereby incorporated by reference.--

- ☒ Priority of Application Nos. 11-043707 and 11-049857 filed in Japan on February 22, 1999 and February 26, 1999 are claimed under U.S.C. § 119.
- ☐ Other:

The filing fee has been calculated as shown below:

			LARGE ENTITY	SMALL ENTITY
BASIC FEE			\$710.00	\$355.00
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<b>TOTAL CLAIMS</b>	26- 20 =	6	X 18 = \$108.00	x 9 = \$
<b>INDEPENDENT CLAIMS</b>	9- 3 =	6	x 80 = \$480.00	x 40 = \$
<input checked="" type="checkbox"/> MULTIPLE DEPENDENT CLAIMS PRESENTED			+ \$270.00	+ \$135.00
<b>TOTAL</b>			1568.00	

- ☒ A check in the amount of \$1,608.00 to cover the filing fee and recording fee (if applicable) is enclosed.
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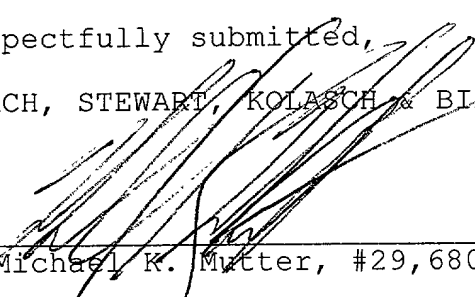
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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

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1163-299P

Attachments

(REV. 09/29/2000)

## TITLE OF THE INVENTION

Packet Generating Method, Video Decoding Method, Media Multiplexer,  
Media Demultiplexer, Multimedia Communication System and Bit Stream  
5 Converter

## CROSS-REFERENCE TO THE RELATED APPLICATION

This application is a continuation of International Application No.  
10 PCT/JP00/01009, whose International filing date is February 22, 2000, the  
disclosures of which Application are incorporated by reference herein.

## BACKGROUND OF THE INVENTION

### 15 1. Field of the Invention

The present invention relates to a video packet generating method, a  
video decoding method, a media multiplexer, media demultiplexer and a  
multimedia communication system which are used in the process of sending  
coded audio, video (moving image) and data packets as a multiplexed stream  
20 from the transmitting side and demultiplexing the multiplexed stream into  
audio, video and data at the receiving side. Furthermore, the present  
invention also pertains to a bit stream converter which permits effective  
conversion of an error resistance syntax according to the channel condition.

### 2. Description of the Prior Art

25 To facilitate a better understanding of the present invention, a  
description will be given first of the case where a coded video stream encoded  
by a conventional video coding system, for example, the ITU-T

Recommendation H. 263 (hereinafter referred to as H. 263), is sent after being multiplexed by the ITU-T Recommendation H. 223 (hereinafter referred to as H. 223). H. 223 defines a multiplexing system by which compress-coded audio, video and data streams are multiplexed into one bit stream for  
 5 transmission.

Fig. 1 depicts the configuration of an H. 233-recommended multiplexing part. As shown, the multiplexing part comprises two hierarchies of an adaptation layer and a multiplexing layer. The adaptation layer input thereto, as packets called AL-SDU (Adaptation Layer Service  
 10 Data Unit), coded streams from an application layer that encodes speech and video, and adds each packet (AL-SDU) with an error correcting code (CRC) and other necessary control information (AL-PDU (Adaptation Layer Protocol Data Unit)). The multiplexing layer inputs thereto, as packets called  
 15 MUX-SDU (Multiplex Service Data Unit), the packets (AL-PDU) of various media from the adaptation layer, and multiplexes them into one bit stream for transmission. The bit stream multiplexed in the multiplexing layer is called MUX-PDU (Multiplex Protocol Data Unit), and comprises a sync flag, a header and information fields for storing the packets (MUX-SDU). Incidentally, the method of dividing the coded streams from the application  
 20 layer into packets (AL-SDU) for processing in the multiplexing part is outside of the scope defined by H. 223.

The above-mentioned multiplexing part is a function at the transmitting side. The receiving side has a demultiplexing part whose function is the inverse of the multiplexing at the transmitting side. In the  
 25 demultiplexing part the bit stream (MUX-PDU) received in a demultiplexing layer is demultiplexed into packets of various media (MUX-SDU), which are output to the adaptation layer (AL-PDU). In the adaptation layer the error

detecting code (CRC) contained in the packet (AL-PDU) is decoded and a check is made for an error in the received packet (AL-PDU). The result of error detection is output to the application layer together with the coded media information stream (AL-SDU) in the packet. It is outside the scope defined  
5 by H. 223 how to use the result of error detection in the application layer.

By the way, the video coding system by H. 263 encodes the input image into blocks of a predetermined size (each of which is comprised of a luminance signal of a 16 by 16 pixel size and a color difference signal of an 8 by 8 pixel size and is called a macroblock); it is considered that the coded  
10 video stream by H. 263 may be rendered into the packet AL-SDU, for example, by a method of combining piece of coded data of plural macroblocks into one packet.

As referred to above, however, it is recommended by H. 223 that the error detecting code be added for each packet AL-SDU and that the  
15 application layer at the receiving side receive the result of error detection together with the packet AL-SDU. Hence, when pieces of coded data of plural macroblocks are combined into one packet AL-SDU, it can be known from the result of error detection that there is an error in any one of the plural macroblocks contained in the packet AL-SDU, but it is impossible to specify  
20 the macroblock in which the error has occurred and the information in which the error has occurred. Accordingly, the prior art fails to practice error concealment by making effective use of the result of error detection received in the application layer.

Next, a description will be given of problems of the moving image  
25 communication between circuits of different error rates.

With the recent broadening the band of a digital transmission line and development of multimedia communication technology, it has become

feasible to transmit moving image signals over various transmission lines. In the field of communication there is in widespread use a teleconference/videophone system that uses the H. 261 moving picture coding system on the ITU-T H. 320 terminal intended for connection to ISDN.

- 5 Further, at the H. 324 terminal assumed to be connected to an analog public network, there are supported not only the H. 261 moving image coding system but also the H. 263 coding system which is higher in coding efficiency than the former coding system.

Moreover, there is recommended, as a terminal assumed to be  
10 connected to an IP network such the Internet, H. 323 that supports H. 261/H263 as is the case with H. 324. Accordingly, since at such wired system terminals there is supported a set of limited moving image coding systems based on ITU Recommendations, the interconnection of different protocols between terminals for limited use is guaranteed to some extent.

15 On the other hand, IMT-2000 is now under study by ITU as a standardization of a multimedia communication system using radio channels, and MPEG-4 of ISO is regarded as a likely moving image coding system. The diffusion of present portable terminals (such as PDC and PHS) are extremely wide, and if multimedia terminals based on IMT-200 comes into  
20 use as substitutes for existing portable telephones, an enormous demand therefor is expected. In such a case, the interconnection between the new multimedia terminals and existing wired system multimedia terminals is important, but this is the interconnection between channels of different error rates, not the interconnection between the existing wired-system circuits.

25 Predicated on the use with radio channels, MPEG-4 contains a function of increasing the error resistance at the video data level; there has not been established so far a technique for effectively converting the function at

the time of interconnecting the multimedia terminals.

The conventional bit stream converter has such a construction as described above, and hence it has a defect that an error resistance syntax cannot effectively be converted between the moving image coding system used in a channel of low error rate and the moving image coding system used in a channel of high error rate according to channel conditions.

### SUMMARY OF THE INVENTION

It is therefore an object of the present invention to provide a media multiplexer, a moving image decoder and a multimedia communication system which permit easy recovery from errors in the decoding of coded video streams by multiplexing their important information into single packets each added with an error detecting code at the transmitting side so that the presence or absence of an error in the important information area can be decided at the receiving side by decoding the error detecting code for each packet.

Another object of the present invention is to provide a bit stream converter which permits effective conversion of an error resistance syntax between moving image coding systems used in communication lines or channels of low and high error rates according to channel conditions.

According to a first aspect of the present invention, there is provided a video packet generating method which divides a coded video stream into plural video segments and generates packets each added with an error detecting code for each video segment. The coded video stream is composed of compressed block coded data, and the block coded data is composed of plural kinds of data elements, the data elements of the same kind being arranged in succession over plural blocks. The coded video stream is



divided, at the point of change in the kind of the data elements arranged in succession, into the packets each added with the error detecting code for each video segment.

With this video packet generating method, it is possible to decide at the receiving side whether an error has occurred in each data sequence by the error detecting code added to each data sequence.

According to a second aspect of the present invention, there is provided a video decoding method which receives a coded video stream, together with an error detection result indicating whether an error is contained in a coded stream in each packet, and decodes the coded video stream. The coded video stream is composed of plural pieces of compressed block coded data. The plural pieces of compressed block coded data are composed of plural kinds of data elements. The data elements of the same kind are arranged in succession over plural blocks, and the coded video stream is divided, at the point of change in the kind of the data elements arranged in succession, into each packet, the each packet being added, for each of the coded video streams, with an error detecting code for obtaining the error detection result. Upon detecting a decoding error at the time of receiving and decoding the coded video stream for each packet, the position of the decoding error in the coded video stream is decided based on an error detection result received and error concealment is selectively performed based on the decided position of the decoding error.

With this video decoding method, it is possible to decide at the receiving side whether an error has occurred in each data sequence by the error detecting code added to each data sequence. By practicing error concealment through utilization of the decision result and the position of an error detected in variable-length decoding, coded video streams can be

decoded with increased error resistance.

According to a third aspect of the present invention, the plural kinds of data elements contain a data stream composed of motion vectors contained in plural blocks and a data stream composed of pieces of texture information  
5 contained in plural blocks. Based on said error detection result received together with each data stream and the position of the decoding error detected in the decoding of each data stream, it is decided whether to perform error concealment using decoded motion vectors or abandon the motion vectors and the texture data and perform error concealment.

10 With this video decoding method, it is possible to decide at the receiving side whether an error has occurred in each data sequence by the error detecting code added to each data sequence. By practicing error concealment through utilization of the decision result and the position of an error detected in variable-length decoding, coded video streams can be  
15 decoded with increased error resistance.

According to a fourth aspect of the present invention, there is provided a video packet generating method which divides a coded video stream into plural video segments and generates packets each added with an error detecting code for each of said plural video segments. The coded video  
20 stream is composed of compressed block coded data, and for each block coded data of plural blocks, header information is coded which contains a unique code indicating the head of each block coded data and its block number. The coded video stream is divided into packets at the point of change between the header information and the compressed block coded data,  
25 the packets being added with the error detecting code for each of the plural video segments.

With this video packet generating method, it is possible to decide at

the receiving side whether an error has occurred in the header information by the unique code for resynchronization and the error detecting code added to the header information.

According to a fifth aspect of the present invention, there is provided a video decoding method which receives a coded video stream, together with an error detection result indicating whether an error is contained in a coded stream in each packet, and decodes the coded video stream. The coded video stream is composed of plural pieces of compressed block coded data, and for each of the compressed block coded data of plural blocks, header information is coded which contains a unique code indicating the head of each block coded data and its block number. The coded video stream is divided into packets at the point of change between the header information and the block coded data, the packets being added, for each of the plural video segments, with an error detecting code for obtaining the error detection result. Upon detecting a decoding error during decoding of the coded video stream received for each packet, the position of resynchronization is decided based on the unique code and the error detection result received together with coded data of the header information, and resynchronization is performed from the bit position of error detection to a unique code indicating the beginning of the next block coded data.

With this video decoding method, it is possible to decide at the receiving side whether an error has occurred in the header information by the unique code for resynchronization and the error detecting code added to the header information. By practicing error concealment through utilization of the decision result and the position of an error detected in variable-length decoding, coded video streams can be decoded with increased error resistance.

According to a sixth aspect of the present invention, there is provided in which packets of coded video streams generated by said video packet generating method according to the first aspect of the invention and packets of audio and data, generated by dividing coded audio and data streams, are multiplexed into one stream for output as a multiplex stream.

With this media multiplexer, it is possible to decide at the receiving side whether an error has occurred in each data sequence by the error detecting code added to each data sequence.

According to a seventh aspect of the present invention, packets of coded video streams generated by the video packet generating method according to the fourth aspect of the invention and packets of audio and data, generated by dividing coded audio and data streams, are multiplexed into one stream for output as a multiplex stream.

With this media multiplexer, it is possible to decide at the receiving side whether an error has occurred in the header information by the unique code for resynchronization and the error detecting code added to the header information.

According to an eighth aspect of the present invention, the multiplex stream by the media multiplexer according to the sixth aspect of the invention is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each packet is decoded to obtain an error detection result. The packets of coded video streams are decoded into video packets by the video decoding method according to the second, third, or fifth aspect of the invention based on the error detection result for each coded video stream concerned.

With this media multiplexer, it is possible to decide at the receiving side whether an error has occurred in each data sequence by the error

detecting code added to each data sequence. Furthermore, the coded video streams can be decoded with increased error resistance.

According to a ninth aspect of the present invention, the multiplex stream by the media multiplexer according to the seventh aspect of the invention is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each packet is decoded to obtain an error detection result. The packets of coded video streams are decoded into video packets by the video decoding method of the second, third, or fifth aspect of the invention based on the error detection result for each video coded string concerned.

With this media demultiplexer, it is possible to decide at the receiving side whether an error has occurred in the header information by the unique code for resynchronization and the error detecting code added to the header information. Furthermore, the coded video streams can be decoded with increased error resistance.

According to a tenth aspect of the present invention, there is provided a multimedia communication system which is provided with a transmitting device and a receiving device. The transmitting device: encodes various pieces of media information, such as audio, video and data, into coded streams; divides the coded streams into various media packets; multiplexes the various media packets, each added with an error detecting code, into a stream; and transmits the multiplex stream over a communication line. The receiving device: receives the multiplex stream sent from the transmitting device; demultiplexes the multiplex stream into the various media packets; and decodes the various media packets in media information decoding parts corresponding thereto. The receiving device comprises means for decoding the error detecting code added for each of the various media packets. The

transmitting device comprises: means for monitoring the error condition of the communication line based on an error detected in the receiving device; and means for generating video packets by dividing a coded video stream in varying unit of division according to the error condition of the communication  
 5 line.

With this multimedia communication system, it is possible to implement packet generation with increased error resistance when the error rate is low and reduction of the redundancy resulting from the packet generation when the error rate is high.

10 According to an eleventh aspect of the present invention, there is provided a multimedia communication system which is provided with a transmitting device and a receiving device. The transmitting device: encodes various pieces of media information, such as audio, video and data, into coded streams; divides the coded streams into various media packets;  
 15 multiplexes the various media packets, each added with an error detecting code, into a stream; and transmits the multiplex stream over a communication line. The receiving device: receives the multiplex stream; demultiplexes the multiplex stream into the various media packets; and decodes the various media packets in media information decoding parts corresponding thereto.  
 20 The receiving device comprises means for decoding the error detecting code added for each of the various media packets. The transmitting device comprises: means for monitoring the error condition of said communication line based on an error detected in the receiving device; and means for multiplexing the media packets of audio, vide and data into one stream while  
 25 changing the ratios of audio, video and data in the multiplex stream according to the error condition of the communication line.

With this multimedia communication system, when the error rate is

high, the media packet of video is divided to lower the rate of the media packet of video contained in one multiplex stream, by which it is possible to prevent the occurrence of many errors in one video packets, and hence provide increased error resistance.

5        According to a twelfth aspect of the present invention, there is provided a bit stream converter which is provided with syntax rule decision means for changing a rule of a coded bit stream syntax. A predetermined one of plural syntax rules is selected by the syntax rule decision means.

10        With this bit stream converter, it is possible to implement an effective conversion of a bit stream according to the line condition.

      According to a thirteenth aspect of the present invention, the syntax rule decision means selects one of the plural syntax rules according to the quality of a communication line over which the converted bit stream is transmitted.

15        With this bit stream converter, it is possible to provide an appropriate coded bit stream in accordance with the condition of the transmission line over which it is sent.

20        According to a fourteen aspect of the present invention, line quality monitoring means is provided for detecting an error of an input bit stream to thereby detect the line quality by the line quality monitoring means.

      With this bit stream converter, it is possible to implement monitoring of the line quality accurately reflecting the error condition of the transmission line.

25        According to a fifteenth aspect of the present invention, the syntax rule decision mans selects predetermined combination of syntaxes, on a one-by-one basis, from four kinds of MPEG-4 error resistance syntaxes which are resynchronization data unit/header multiplex/data division/two-way

decodable code words.

With this bit stream converter, it is possible to provide an efficient bit stream according to the line condition while achieving a balance between the degree of error resistance of the moving image coded bit stream to be sent  
 5 over a transmission line of high error rate and the transmission efficiency as a whole.

According to a sixteenth aspect of the present invention, there is provided a bit stream converter which comprises: syntax analysis means which analyzes a first bit stream according to a predetermined rule, separates  
 10 the first bit stream into coded data and detects an analysis error; and coded data converting means which converts coded data, lost by the analysis error, into an error-free value on a syntax based on a moving image coding method at the destination of a second bit stream.

With this bit stream converter, a terminal which receives a coded bit  
 15 stream based on the moving image coding method operates on the predication that it is connected to a high quality transmission line; hence, it is possible to perform decoding while suppressing deterioration of the picture quality even if no particular measures are taken against errors.

According to an eighteenth aspect of the present invention, there is  
 20 provided a bit stream converter which comprises: syntax analysis means which analyzes a first bit stream according to a predetermined rule, separates the first bit stream into coded data and detects an analysis error; and coded data converting means which converts coded data at the position of detection of the analysis error or in its vicinity into data recognizable as an analysis  
 25 error on a syntax based on a moving image coding system at the destination of transmission.

With this bit stream converter, it is possible to provide an appropriate



coded bit stream in accordance with the condition of the transmission line over which it is sent. Moreover, a terminal which receives a coded bit stream based on the moving image coding method operates on the predication that it is connected to a high quality transmission line; hence, it is possible to  
5 perform decoding while suppressing deterioration of the picture quality even if no particular measures are taken against errors.

### BRIEF DESCRIPTION OF THE DRAWINGS

Other objects, features and advantages of the present invention will  
10 become more apparent from the following description taken in conjunction with the accompanying drawings, in which:

Fig. 1 is a diagram showing the configuration of an H. 223-recommended multiplexing part

Fig. 2 is a block diagram illustrating the configuration of a  
15 transmitting device of a multimedia communication system according to a first embodiment of the present invention

Fig. 3 is a diagram depicting the structure of a coded video stream that is output from an MPEG-4 video coding part 1;

Fig. 4 is a diagram for explaining how to divide the coded video  
20 stream into packets (AL-SDU);

Fig. 5 is a block diagram depicting the configuration of a receiving device of the multimedia communication system according to the first embodiment;

Fig. 6 is a block diagram depicting the configuration of an MPEG-4  
25 video decoding part 22;

Fig. 7 is a diagram showing an example of decoding when a bit error is contained in the coded video stream;

Fig. 8 is a flowchart for explaining decoding in the MPEG-4 video decoding part 22;

Fig. 9 is a diagram showing an example of macroblock data for use in a second embodiment of the present invention;

5 Fig. 10 is a diagram for explaining a scheme for mapping a coded video stream into packets (AL-SDU) according to the second embodiment;

Fig. 11 is a flowchart depicting the decoding procedure according to the second embodiment;

10 Fig. 12 is a diagram showing an example of the detection of an error in the decoding of VP header information;

Fig. 13 is a diagram showing the detection of an error at the address of a macroblock contained in a VP header;

15 Fig. 14 is a block diagram illustrating the configuration of a receiving device of a multimedia communication system according to a fourth embodiment of the present invention;

Fig. 15 is a block diagram illustrating the configuration of a transmitting device of a multimedia communication system according to the fourth embodiment;

20 Fig. 16 is a block diagram illustrating the configuration of a multiplexing part 5 according to the fourth embodiment;

Fig. 17 is diagram showing a system in which a bet stream converter according to a fifth embodiment is intended to use;

Fig. 18 is a block diagram illustrating the internal configuration of the bit stream converter according to the fifth embodiment;

25 Fig. 19 is a block diagram showing the internal configuration of a channel quality monitoring part of the bit stream converter according to the fifth embodiment;

Fig. 20 is a diagram depicting the structure of an MPEG-4 video packet;

Fig. 21 is a flowchart showing an MPEG-4 data partitioning syntax

Fig. 22 is a flowchart showing the operation procedure of the bit  
5 stream converter according to the fifth embodiment;

Fig. 23 is a flowchart showing another operation procedure of the bit  
stream converter according to the fifth embodiment;

Fig. 24 is a flowchart showing still another operation procedure of the  
bit stream converter according to the fifth embodiment;

10 Fig. 25 is a block diagram depicting the internal configuration of a bit  
stream converter according to a sixth embodiment of the present invention;

Fig. 26 is a block diagram depicting the internal configuration of a bit  
stream converter according to a seventh embodiment of the present invention;

Fig. 27 is a block diagram illustrating the internal configuration of a  
15 system converter in the bit stream converter according to the seventh  
embodiment;

Fig. 28 is a block diagram illustrating the internal configuration of a  
system converter according to an eighth embodiment of the present invention;

Fig. 29 is a flowchart showing the operation procedure of a bit stream  
20 converter according to the eighth embodiment;

Fig. 30 is a block diagram illustrating the internal configuration of a  
system converter in a bit stream converter according to a ninth embodiment of  
the present invention;

Fig. 31 is a block diagram illustrating a system converter of a bit  
25 stream converter according to a tenth embodiment of the present invention;  
and

Fig. 32 is a flowchart showing the operation procedure of the bit

stream converter according to the tenth embodiment.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

A description will be given, with reference to the accompanying  
5 drawings, of the preferred embodiments of the present invention.

### **EMBODIMENT 1**

Fig. 2 is a block diagram illustrating a transmitting apparatus of a multimedia communication system according to a first embodiment (Embodiment 1) of the present invention in which a coded video stream, encoded by the MPEG-4 coding system (ISO/IEC 14496-2), is multiplexed by H. 223 together with coded speech and data streams at the transmitting side and transmitted therefrom and at the receiving side the multiplexed stream is demultiplexed into the original speech, video and data. Reference numeral 1  
10 denotes an MPEG-4 video coding part; 2 denotes a speech coding part; 3 denotes a data coding part; 4 denotes a packetizing part; 5 denotes a multiplexing part; 6 denotes a coded video stream packetizing part; 7 denotes a coded speech stream packetizing part; 8 denotes a coded data stream packetizing part; 9 denotes an input image signal; 10 denotes an input speech signal; 11 denotes an input data signal; 12 denotes a coded video stream; 13 denotes a coded speech stream; 14 denotes a coded data stream; 15 denotes a packet of the coded video stream; 16 denotes a packet of the coded speech stream; 17 denotes a packet of the coded data stream; and 18 a multiplexed stream.

25 The MPEG-4 video coding part 1 compress-encodes the input image signal 9 and outputs the coded video stream 12. The speech coding part 2 compress-codes the input speech signal 10 by an arbitrary speech coding

scheme and outputs the coded speech stream. The data coding part 3 compress-codes the input data signal 11, such as a signal for video and audio synchronization or control signal, or changes the data structure of the input data signal 11 and outputs the coded data stream 14.

5 In the packetizing part 4, the coded video, speech and data streams 12, 13 and 14 are broken into packets (AL-SDU) 15, 16 and 17, respectively, for processing in the multiplexing part 5. The thus generated packets (AL-SDU) 15, 16 and 17 are each added with an error detecting code or the like, and in the multiplexing part 5 the packets of the coded video, speech and data  
10 streams (MUX-SDU) are multiplexed into the multiplexed stream (MX-PDU) 18 for output. The operation of the multiplexing part 5 is such as described previously with respect to the prior art.

A detailed description will be given of the packetizing part 4 characteristic of this embodiment.

15 While the packetizing part 4 comprises the coded video, speech and data stream packetizing parts 6, 7 and 8, the operation of the coded video stream packetizing part 6, in particular, will be described below.

Now, let it be assumed that the coded video stream 12 provided from the MPEG-4 video coding part 1 is coded using the data partitioning scheme  
20 defined by MPEG-4 as described later on.

A description will be given first of the structure of the coded video stream 12 that is output from the MPEG-4 coding part 1.

As depicted in Fig. 3, each frame of the input image (called VOP (Video Object Plane) in MPEG-4) is encoded for each resynchronization unit  
25 called a video packet.

The video packet is a set of plural macro blocks in the image space. On the coded video stream, as shown in Fig. 3(a), the video packet is

composed of coded data in units of macro blocks (macro block data), a unique word for resynchronization (a resynchronization marker) and header information necessary for resynchronization (a VP header). In the first video packet of each frame, a unique code (a VOP start code) indicating the front  
 5 end of the frame and header information (VOP header) necessary for decoding coded data in each frame are coded in place of the resynchronization marker and the VP header, respectively, as depicted in Fig. 3(a).

What is intended to mean by “data partitioning” is to provide such a structure as shown in Fig. 3(b) in which when the macro block data in the  
 10 video packet contains information about  $n$  macro blocks, motion vectors and pieces of header information on the  $n$  macro blocks and pieces of texture information on the  $n$  macro blocks are arranged at different locations. The boundary between the motion vector and the texture information is defined by a unique word referred to as a motion marker.

15 The coded video stream packetizing part 6 divides the coded video stream of the Fig. 3 structure into packets (AL-SDU) for processing in the multiplexing part 5 and outputs the packets (AL-PDU).

Fig. 4 depicts how to divide the coded video stream 15 into the packets (AL-SDU) to generate therefrom the packets (AL-PDU).

20 As depicted in Fig. 4, the input coded video stream 15 is divided into the video packets shown in Fig. 3(a), and a set of motion information and macro block header information, a set of motion marker and texture information and a set of resynchronization marker and VP header that are contained in the respective video packets are each mapped as one packet (AL-  
 25 SDU). Incidentally, since the front-end video packet has added thereto the VOP start code and the VOP header in place of the resynchronization marker and the VP header as referred to above, the VOP start code and the VOP

header are mapped as one packet (AL-SDU) as a substitute for the set of resynchronization marker and VP header.

The length of each packet (AL-SDU) needs to be an integral multiple of 8 bits, but the information (the motion information and the macro block header information, the motion marker and the texture information, and the resynchronization marker and the VP header) to be mapped as one packet is not always an integral multiple of 8 bits. Accordingly, there are cases where each of the sets of motion information and macro block header information, motion marker and texture information, and resynchronization marker and VP header are not accurately mapped as one packet but the motion information is partly contained in the packet in which the resynchronization marker and the VP header.

Since the length of one video packet formed by each packet (AL-SDU) is an integral multiple of 8 bits, however, there is no possibility of data of different video packet being mapped into the same packet.

The multiplexing part 5 adds, as depicted in Fig. 4, each packet (AL-SDU) with an error detecting code (CRC) and, if necessary, control information, generating the packet (AL-PDU). In some cases, each packet (AL-SDU) is subdivided into plural packets, which are each added with an error correcting code and then added with the error detecting code (CRC) and the control information to generate the packet (AL-PDU).

Next, a description will be given of the construction and operation of a receiving apparatus which receives the multiplexed stream sent from the transmitting apparatus and decodes it into various pieces of media information such as speech, video and data.

Fig. 5 is a block diagram of the receiving apparatus for use in the multimedia communication system. Reference numeral 25 denotes a

multiplexed stream; 26 denotes a video-media packet for video, 27 denotes a  
 speech-media packet; 28 denotes a data-media packet; 29 denotes a packet of  
 the coded video stream; 30 denotes the result of error detection in the coded  
 video stream; 31 denotes a packet of the coded speech stream; 32 denotes the  
 5 result of error detection in the coded speech stream; 33 denotes a packet of the  
 coded data stream; 34 denotes the result of error detection in the coded data  
 stream; 35 denotes a decoded image signal 36 denotes a decoded speech  
 signal; and 37 denotes a decoded data signal.

A demultiplexing part 20 demultiplexes the multiplexed stream 25  
 10 into speech- video- and data media packets (MUX-SDU) through utilization  
 of a synchronization flag and header information contained in the stream 25.  
 The media packets 26 to 28 are fed into an error detecting/packet separating  
 part 21, in which error detecting codes contained in the media packets 26 to  
 28 are decoded, and the packets (AL-SDU) 29, 31 and 33 from which the  
 15 error detecting codes and the control information have been removed, and the  
 decoded error detection results 30, 32 and 34 are provided to information  
 source decoding parts 22 to 24 respectively corresponding to them.

When the media packet contains a code for error correction, the code  
 is decoded for correction of an error in the media packet. Whether the media  
 20 packet still contains an error remaining in excess of the error correcting  
 capability is checked by decoding an error detecting code that is contained in  
 the media packet after the decoding the code for error correction. In this  
 instance, the packets (AL-SDU) 29, 31 and 33, obtained by removing the  
 error correcting codes, the error detecting codes and the control information  
 25 from the media packets 26 to 28, and the results of error detection 30, 32 and  
 34 are provided to information source decoding parts 22 to 24 respectively  
 corresponding to them. That is, the packet 29 of the coded video stream is



fed to an MPEG-4 video decoding part 22; the packet 31 of the coded speech stream is fed to a speech decoding part 23; and the packet 33 of the coded data stream is fed to a data decoding part 24.

Next, the construction and operation of the MPEG-4 video decoding  
 5 part 22 in this embodiment will be described below.

Fig. 6 illustrates in block form the configuration of the MPEG-4 video decoding part 22. Reference numeral 40 denotes a variable-length decoding  
 10 part; 41 denotes a de-quantizing part; 42 denotes a motion compensating part; 43 denotes a memory; 44 denotes a compensated image generating part; 45  
 denotes an inverse DCT part; 46 denotes an adding part; 47 and 48 denote a  
 switching parts; 49 denotes decoded block data; 50 decoded additional  
 information; 51 denotes a motion vector; 52 denotes a reference image; 53 a  
 decoded prediction error signal; 54 denotes a decoded image; 55 denotes a  
 predicted image; 56 denotes an error detection flag; and 57 denotes a  
 15 compensated image. Reference numeral 35 denotes a decoded image signal  
 that is output from the MPEG-4 video decoding part 22.

The packet of the coded video stream 29 is decoded with a variable  
 length in the variable-length decoding part 40 based on the syntax defined by  
 MPEG-4.

20 The variable-length decoding part 40 is equipped with mechanism for  
 detecting an error in the decoding of the coded video stream 29; when the  
 decoded value is incorrect, or when the continuation of decoding is  
 impossible due to loss of synchronization, it is detected as a decoding error.  
 Upon detecting such an error, the variable-length decoding part 40 provides  
 25 the error detection flag 56 to the compensated image generating part 44. In  
 the case of decoding the packet of the coded video stream encoded by the data  
 partitioning function as depicted in Fig. 3(c), when an error is detected not in

the motion information area but in the texture information area (see Fig. 4), the error detection flag 56 is provided to the switching part 48.

The motion vector 51 decoded in the variable-length decoding part 40 is fed to the motion compensation part 42. Based on the motion vector 51, the motion compensation part 42 extracts the predictive image 55 from the reference image 52 stored in the memory 43. The predictive image 55 is provided to the switching part 48, through which it is fed to the compensated image generating part 44 or adding part 46, depending upon whether the switching part 48 has received the error detection flag 55 from the variable-length decoding part 40.

The block data 49 decoding in the variable-length decoding part 40 is provided via the de-quantizing part 41 to the inverse DCT part 45, from which it is provided as the decoded prediction error signal 53. The decoded prediction error signal 53 is applied to the adding part 46, wherein it is added to the predictive image 55 from the motion compensation part 42 to obtain the decoded image signal 54. Incidentally, when the macro block type contained in the decoded additional information 50 indicates intra-coding, a "0" signal is chosen in the switching part 47 and the decoded prediction error signal 53 is not added with the predictive image 55 and becomes the decoded image 54. The decoded image 54 mentioned herein means a decoded macro block, which is composed of four luminance blocks and two color difference blocks. The decoded image 54 is written in the memory 43 so that it is used as a reference image of the next frame.

Now, a detailed description will be given of the variable-length decoding part 40 characteristic of this embodiment.

When the receiving apparatus has received the multiplexed stream sent from the transmitting apparatus shown in Fig. 2, the coded video stream

29 is input to the variable-length decoding part 40 of the MPEG-4 video decoding part 22 in such a manner that the motion information and the macro block header information, the motion marker and the texture information, and the resynchronization marker and the VP header, contained in the video packets, are provided in the form of single packets, respectively, as depicted in Fig. 4. In this case, each packet is accompanied by the error-detection result 30 indicating whether an error is contained in the packet.

The decoding of the coded video stream 29 with a bit error contained therein will be described below.

Fig. 7 shows, by way of example, the result of error detection in the case where a bit error occurs in the information area of motion information and macro block header. In the illustrated example, the coded moving image string 29 is decoded normally halfway through the texture information although the bit error has occurred in the information area of motion information and macro block header, and the bit error is detected in the texture information area.

In such a case, according to the prior art, the result of decoding until the detection of an error is regarded as correct and concealment of the error is started at the position where the error was detected. Accordingly, in the case of Fig. 7, the next resynchronization marker is searched, and at the same time the coded stream from the position where the error was detected to the resynchronization is abandoned, and the macro block at the position where the error was detected and the subsequent information area are compensated for by macro block data which is generated using the motion vector decoded before the motion marker.

In the case of Fig. 7, however, since the error has occurred in the information area of motion information and macro block header, the

generation of a compensated image using the incorrectly decoded motion vector and macro block header information will seriously deteriorate the subjective picture quality of the decoded image.

To avoid this, the MPEG-4 video decoding part 22 of this embodiment  
 5 practice concealment of the error through utilization of the error-detection result 30 which is input together with the packet of the coded video stream 29.

Fig. 8 is a flowchart for explaining the decoding procedure in the MPEG-4 video decoding part 22.

As depicted in Fig. 7, the video decoding part 22 first detects the  
 10 resynchronization marker indicating the head of the video packet (step ST1 “Yes”), then decodes the VP header information (step ST2), and at the same time makes a check for an error in the decoding of the VP header (step ST3). If an error is detected in the decoding of the VP header information (step ST3 “Yes”), the video decoding part 22 abandons the entire data of the video  
 15 packet (VP) having the VP header, and practices concealment (step ST4).

On the other hand, when no error is detected in the decoding of the VP header information (step ST3 “No”), the video decoding part 22 decodes the motion information and macro block header information contained in the next packet (AL-SDU) (see Fig. 4) (step ST5), and at the same time makes a check  
 20 for a decoding error (step ST6). The decoding of the motion information and the macro block header information in step ST5 and the error check in the step ST6 are repeated until the detection of the motion marker contained in the next packet (AL-SDU) (step ST7 “Yes”).

If an error is detected in the decoding of the motion information and  
 25 the macro block header information (step ST6 “Yes”), the video decoding part 22 abandons the entire data of the video packet and practices concealment (step ST4) as in the case of having detected an error in step ST3.

On the other hand, in the case where no error is detected (step ST6 “No”) and the motion marker contained in the next packet (AL-SDU) is detected (step ST7 “Yes”), the video decoding part 22 decodes the texture information contained in that packet (AL-SDU) (step ST8) and makes a check  
 5 to see if a decoding error has been detected (step ST9).

In the case of Fig. 7, although an error has occurred in the decoding of the motion information and the macro block header in step ST5, the error is detected by step ST9 in the decoding of the texture information in step ST8.

To deal with this, according to this embodiment, when an error is  
 10 detected by step ST9 in the decoding of the texture information in step ST8 (step ST9 “Yes”), the video decoding part 22 makes a check to determine which of the packet of motion information and macro block header information and the packet of texture information contains the error, based on the error-detection results 30 accompanying the packet of motion information  
 15 and macro block header information and the packet of texture information, respectively (step ST10).

In the case where the error-detection result 30 received together with the packet of motion information and macro block header information indicates the presence of an error but the error-detection result 30 received  
 20 along with the packet of the texture information indicates the absence of an error (step ST10 “Yes”), there is a strong possibility that an error occurred in the decoding of the motion information and the macro block header information although an error has been detected in the decoding of the texture information area as shown in Fig. 7.

25 In this instance, the video decoding part 22 provides to the compensated image generating part 44 the error detection flag 56 which indicates abandoning the entire data of the video packet (VP), that is, the

motion information, the macro block header information and the texture information, and practicing concealment to generate a compensated image (step ST12).

In the case where the error-detection 30 received along with the packet  
 5 of motion information and macro block header information indicates the absence of an error and the error-detection result received along with the packet of texture information indicates the presence of an error (step ST10 “No”), it is very likely that the motion information and the macro block header information decoded earlier than the motion marker are correct and  
 10 that an error has occurred in the texture information area where an error was detected during decoding.

In such an instance, the video coding part 22 provides to the compensated image generating part 44 and the switching part 48 the error detection flag 56 which indicates that a predictive image obtained using the  
 15 motion vector decoded earlier than the motion marker be used for concealment to compensate the decoded image for the error.

And, when the error detection flag 56 indicates that all pieces of the data contained in the video packets (VP), that is, the motion information, the macro block header information and the texture information, were abandoned  
 20 in step ST12, the compensated image generating part 44 generates the compensated image 57 using information contained in the reference image. When the error detection flag 56 indicates that the motion information was decoded correctly in step ST11, the compensated image generating part 44 generates the compensated image 57 using the predictive image 55 that is  
 25 provided from the switching part 48. The thus generated compensated image 57 is written in the memory 43 for use as a reference image in the next frame.

As described above, according to this embodiment, the coded video stream is broken down into packets each corresponding to a different data stream and each packet is transmitted together with an error detecting code. Accordingly, at the receiving side the presence or absence of an error in each data stream can be detected based on the error detecting code added to the data stream, and by performing concealment through the use of the error-detection result together with the position of an error detected during variable-length decoding, the coded video stream can be decoded with increased resistance to errors.

## EMBODIMENT 2

This embodiment (Embodiment 2) concerns modifications of the coded video stream packetizing part 6 and the MPEG-4 video decoding part 1 used in Embodiment 1.

In this embodiment no particular limitations are imposed on the structure of the macro block data of the coded video stream that is output from the MPEG-4 video coding part 1. That is, the coded video stream shown in Fig. 3(a) may be the macro block data encoded using the data partitioning scheme defined by MPEG-4 as depicted in Fig. 3(b), or macro block data of a structure in which the macro block header, the motion information and the texture information are arranged for each of n macro blocks as depicted in Fig. 9. This embodiment does not impose any particular limitations on the macro block data structure as mentioned above, but the following description will be given on the assumption that the macro block data has the structure shown in Fig. 9.

The operation of the coded video stream packetizing part 6 in this embodiment will be described below.

The coded video stream packetizing part 6 inputs therein the coded video stream of the structure depicted in Fig. 3(a) and divides it into packets (AL-SDU) for processing in the multiplexing part.

Fig. 10 is a diagram for explaining how to map the coded video stream into the packets (AL-SDU) in this embodiment. As shown, the VOP start code and the VOP header, the macro block data, and the resynchronization marker and the VP header, all contained in the coded video stream, are mapped into single packets (AL-SDU), which are each added with control information and an error detecting code (CRC) to form a packet (AL-PDU).

When the macro block data is encoded using the data partitioning scheme as depicted in Fig. 3(b), it may be mapped as described previously with reference to Fig. 4. The macro block data may also be mapped into one packet, but in this embodiment the macro block data is mapped into two packets as shown in Fig. 10. Since the length of each packet (AL-SDU) is a multiple of 8 bits as is the case with Embodiment 1, information about the macro block data may sometimes be contained, for example, in the packet that contains the VOP start code and the VOP header. Though not shown in Fig. 10, the packet containing the resynchronization marker and the VP header is followed by a macro block header for the resynchronization maker and the VP header.

Next, the operation of the MPEG-4 video decoding part 22 in this embodiment will be described below on the assumption that a bit error is contained in the received coded video stream packetized as shown in Fig. 10.

Fig. 11 is a flowchart for explaining the decoding procedure of the video decoding part 22 in this embodiment.

The video decoding part 22 makes a check to determine whether the VOP start code is detected (step ST20), and if so (step ST20 “Yes”), decodes



the VOP header information (step ST21), then decodes the macro block data of the next packet (AL-SDU) (step ST22), and at the same time makes a check to see if an error is detected in its decoding (step ST23). And, when an error is detected in the decoding of the macro block data (step ST23 “Yes”),

5 the video decoding part 22 continues concealment of the macro block data until the next VOP start code or resynchronization marker is detected (step ST24).

On the other hand, in the case where no error is detected in the decoding of the macro block data (step ST23 “No”) and the resynchronization marker of the next packet (AL-SDU) is detected (step ST25 “Yes”), the video decoding part 22 decodes the VP header information of that packet (step ST26) and makes a check to determined whether an error is detected in its decoding (step ST27).

10

And, when no error is detected in the decoding of the VP header information (step ST27 “No”), the video decoding part 22 decodes the macro block data of the next packet (AL-SDU) (step ST28), and makes a check to see if an error is detected in its decoding (step ST29). When an error is detected in the decoding of the macro block data (step ST29 “Yes”), the video decoding part 22 continues concealment of the macro block until the next

15

20 VOP start code or resynchronization marker is detected (step ST30) as in the case of step ST24.

On the other hand, in the case where no error is detected in the decoding of the macro block data (step ST29 “No”) and the next resynchronization marker is detected (step ST25 “Yes”), the video decoding

25 part 22 decodes the VP header information of that macro block (step ST26) and makes a check to determine whether an error is detected in its decoding (step ST27).

Next, a description will be given, with reference to Fig. 12, of the case where an error is detected in step ST27 during the decoding of the VP header information in step ST26.

Fig. 12 shows, by way of example, the case where an error is detected  
5 in step ST27 during the decoding of the VP header information in step ST26.

Now, consider the case where an error is contained in the VOP start code. In order to detect the error earlier than the VOP start code and establish resynchronization, a search is made for the next VOP start code. Since the VOP start code to be detected is lost by the error as depicted in Fig.  
10 12, no VOP start code is detected and the next resynchronization marker is detected as the position of resynchronization; however, the VOP header and the macro block data between them is not decoded.

(1) When header extension information is contained in VP header information:

15 In the case where the VP header is decoded next and it contains header extension information as depicted in Fig. 12, the header extension information is compared with the VOP header information already decoded. In this case, since no VOP header has been decoded due to an error, the VOP header information for comparison is information of the preceding VOP header.  
20 Accordingly, in step ST27 an error that the header extension information and the VOP header information differ is detected during the decoding of the VP header information.

With the conventional MP+EG-4 video decoding part, however, it is difficult to decide whether the VOP header information is not correctly  
25 decoded or the header extension information is not correctly decoded due to an error.

In the MPEG-4 video decoding part 22 in this embodiment,



the video packet before resynchronization and the macro block address of the video packet detected after resynchronization; when the latter macro block address is smaller than the former, an error is detected.

With the conventional MPEG-4 video decoding part, it is difficult to  
 5 decide whether no VOP start code has been detected or an error has occurred in the macro block address of the VP header. If it is decided in error that an error has occurred in the macro block address of the VP header, decoding is resumed at the point where the resynchronization marker of the video packet whose macro block address was larger than that of VP before  
 10 resynchronization was detected. In this case, although the detected video packet is in VOP different from that before resynchronization, the decoded image is written in the same VOP as that of the video packet before resynchronization, seriously deteriorating the subjective picture quality.

In the MPEG-4 video decoding part 22 in this embodiment, when an  
 15 error that the macro block address is smaller than the macro block address of the video packet decided before resynchronization is detected, the error-detection result received together with the packet of resynchronization and VP header information is checked. If the error-detection result indicates that there is no error, the possibility of the VOP start code having not been  
 20 detected in error is stronger than the possibility of the macro block address of the VP header information having been decoded in error, and hence the coded string is resynchronized from the VOP start code of the current VOP to the next VOP start code or the resynchronization marker of the video packet that has the VP header containing the header extension information. With this  
 25 technique, it is possible to avoid serious deterioration of the picture quality which would otherwise be caused by writing the VP data of a wrong video object plane (VOP) when no VOP start code is not detected.

(3) When error is detected in macro block address contained in VP header:

Next, consider the case where an error is detected in the macro block address contained in the VP header. This situation arises when there is no continuity between the last macro block address of the immediately  
 5 previously decoded video packet and the macro block address contained in its VP header (the macro block address at the head of the video packet). It is considered that such discontinuity occurs in the two cases mentioned below.

(1) When the macro block data decoded is wrong in number because of the occurrence of an error during decoding of the macro block data in the  
 10 immediately preceding video packet (Fig. 13(a)).

(2) When an error occurs in the macro block address of the VP header (Fig. 13(b)).

With the conventional MPEG-4 video decoding part, it is difficult to decide in which of the cases (1) and (2) the error is detected. If it is decided  
 15 in error that the error is detected in the case (1) although it is actually in the case (2), concealment is carried out abandoning correctly decoded data of the immediately preceding video packet and decoding is resumed from the video packet having its macro block address decoded in error. In this case, a decoded image is written in the wrong macro block address, incurring a  
 20 serious deterioration of the picture quality; furthermore, since the correctly decoded image data is also abandoned, the picture quality suffers a severe deterioration. In the opposite case, the VP data having correctly decoded VP header information is abandoned and VP header information decoded in error is written intact as a decoded image, also causing a serious deterioration in the  
 25 picture quality.

In view of the above, the MPEG-4 video decoding part 22 according to this embodiment specifies the cause of the above-mentioned discontinuity

between the macro block addresses through utilization of the error-detection result received along with the packet of resynchronization marker and VP header information as depicted in Fig. 10.

More specifically, when the error-detection result indicates the presence of an error, this means that an error is contained in the VP header, since the resynchronization marker has already been detected correctly. Accordingly, the possibility of case (2) is stronger than the case (1). Then, the next resynchronization marker or start code is searched and used as the position of resynchronization.

When the error-detection result does not indicate the presence of an error in the VP header, the possibility of the case (1) is stronger than the case (2). Then, concealment is practiced for the immediately preceding video packet and decoding is resumed at the VP header. With this technique, it is possible to prevent the above-mentioned severe deterioration of the picture quality experienced in the prior art when discontinuity arises in the macro block address contained in the VP header.

As described above, according to this embodiment, when the coded video stream is broken down into packets for processing in the multiplexing part, such unique codes for resynchronization as the VOP start code and the resynchronization marker and such header information such as the VOP header and the VP header are mapped into one packet, and such packets are each transmitted together with the error detecting code. Accordingly, at the receiving side is possible decide whether an error has occurred in the header information based on the unique codes for resynchronization and the error detecting code added to the header information, and by performing concealment through the use of the result of decision together with the position of error detected during variable-length decoding, the coded video

stream can be decoded with increased resistance to errors.

### EMBODIMENT 3

This embodiment (Embodiment 3) is directed to another modification  
5 of the coded video stream packetizing part used in Embodiment 1.

The coded video stream packetizing part 6 in this embodiment is  
characterized in that the resynchronization marker, which is a particularly  
important one of the pieces of VP header information, and the macro block  
address are mapped into one packet (AL-SDU). That is, As referred to  
10 previously in Embodiment 2, the macro block address contained in the VP  
header information represents absolute position information of the macro  
block at the head of the video packet. Accordingly, when the macro block  
address is wrong, the decoded macro block is placed at a wrong position in  
the picture, resulting in the decoded image suffering a serious deterioration in  
15 image quality. To avoid this, in the coded video stream packetizing part 6  
according to this embodiment, the resynchronization marker in the VP header  
information and the macro block address are mapped into one packet (AL-  
SDU).

In such an instance, the MPEG-4 video decoding part 22 in this  
20 embodiment receives the packet of resynchronization marker and macro block  
address, together with an error-detection result indicating the presence or  
absence of an error in the packet. In the decoding process, if the error-  
detection result indicates the presence of an error when the resynchronization  
marker is decoded, it indicates the occurrence of an error in the macro block  
25 address. Then, in this embodiment the video packet concerned is abandoned  
and resynchronization is started at the next video packet, by which it is  
possible to prevent a serious deterioration of the picture quality which would

caused by placing the decoded image at a wrong position in the picture.

#### EMBODIMENT 4

This embodiment is directed to a multimedia communication system  
5 which is characterized in that the packet generation and the media  
multiplexing rate in the transmitting apparatus are controlled based on the  
result of error detection in the receiving apparatus.

Fig. 14 illustrates in block form the receiving apparatus of the  
multimedia communication system according to this embodiment. The parts  
10 corresponding to those in the receiving apparatus of Embodiment 1 shown in  
Fig. 5 are identified by the same reference numerals, and no description will  
be repeated in connection with them. Reference numeral 60 denotes the  
result of error detection.

Fig. 15 illustrates in block form the transmitting apparatus of the  
15 multimedia communication system according to this embodiment. The parts  
corresponding to those in the transmitting apparatus of Embodiment 1 shown  
in Fig. 2 are identified by the same reference numerals, and no description  
will be repeated in connection with them. Reference numeral 60 denotes the  
result of error detection which is provided from the error detection/packet  
20 separation part 21 of the receiving apparatus; 61 denotes an error monitoring  
part; and 62 denotes an error rate.

Next, the operation of this embodiment will be described below.

In the receiving apparatus side of Fig. 14, the packet separation/error  
detection part 21 sends, as the result of error detection 60 in the receiving  
25 apparatus, the result of error detection 30 made in the video packet (the coded  
video stream), the result of error detection 32 in the speech packet (the coded  
speech stream) and the result of error detection 34 made in the data packet



(the coded data stream) to the transmitting apparatus side.

In the transmitting apparatus side of Fig. 15, the error monitoring part 61 monitors the error-detection result 60 sent from the receiving side, and provides the error rate 62 in the receiving apparatus to the packet generating part 4 and the multiplexing part 5.

In the packet generating part 4 the packet assembling scheme is changed according to the error rate 62 provided from the error monitoring part 61. For example, when the error rate is high, a packet assembling scheme of high error resistance is used because of a channel condition under which a transmission error is likely to occur. Concretely, the start code and the header information are mapped as one packet as described previously in respect of Embodiments 1 and 2 (see Figs. 4 and 10). In a channel of a higher error rate, the motion information and the texture information are mapped as different packets as referred to previously with respect to Embodiment 1 (see Fig. 4), by which the error resistance can be increased. On the other hand, in a channel of a low error rate, since a transmission error is not likely to occur, for example, the video packet is mapped intact as one packet, or plural video packets is mapped as one packet, by which it is possible to reduce the redundancy resulting from the packet generation.

In the multiplexing part 5, the pattern of the multiplexed packet into which various media packets from the packet generating part 4 are multiplexed is changed in accordance with the error rate 62 which is provided from the error monitoring part 61.

Fig. 16 illustrates in block form the multiplexing part 5 in this embodiment. Reference numeral 70 denotes a multiplexing pattern selecting part; 71 denotes a multiplexed packet generating part; 72 denotes a sync flag/header information adding part; 73 denotes an error detecting code

adding part; 74 denotes a multiplexing pattern; and 75 denotes a multiplexed packet. Reference numeral 62 denotes the error rate which is provided from the error monitoring part 61.

Next, the operation of the multiplexing part 5 shown in Fig. 16 will be described below.

In the first place, based on the error rate 62, the multiplexing pattern selecting part 70 selects one multiplexing pattern 74 from a table in which there are defined plural multiplexing patterns. The multiplexing pattern has defined therein information according to which, for example, only the media packet of speech is rendered into one multiplexed stream, or only the media packet of video is rendered into one multiplexed stream, or packets into which media packets of speech and video are multiplexed at a predetermined ratio between them are rendered into one multiplexed stream. With this definition, it is also possible to divide the video media packet (MUX-SDU) into plural elements and render them into different multiplexed streams.

For example, in a channel of a high error rate, the rate of the video media packet contained in one multiplexed stream is decreased. That is, the media packet of video is divided into plural elements so that they are contained in as many multiplexed streams as possible. The reason for this is that the influence of an error in video is large; especially in the case where every piece of information in one video packet are abandoned due to a burst error, various error tools adopted by MPEG-4 cannot effectively be used. To prevent this, the rate of the media packet of video in one multiplexed stream is reduced so that errors will not occur intensively in one video packet.

For a channel of a low error rate, many pieces of information are rendered into one multiplexed stream with a view to reducing the redundancy that is attributable to the header information of the multiplexed stream.

The multiplexing pattern 74 selected in the multiplexing pattern selecting part 70 is sent to the multiplexed packet generating part 71 and the sync flag/header information adding part 72. Based on the multiplexing pattern 74, the multiplexed pattern generating part 71 assembles the packet 15 of the coded video stream, the packet 16 of the coded speech stream and the packet 17 of the coded data stream into one multiplexed packet 75. The multiplexed packet 75 is added with a sync flag and header information in the sync flag/header information adding part 72. The multiplexing pattern 74 selected in the multiplexing pattern selecting part 70 is multiplexed as the header information. In the error detecting code adding part 73 the multiplexed packet 75 is added with an error detecting code to make the header information error-resistant, thereafter being output as the multiplexed stream 18.

As described above, according to this embodiment, the packet assembling scheme in the packet generating part 4 is chosen according to the error rate, by which it is possible to implement the packet generation with increased error resistance when the error rate is high and to reduce the redundancy by the packet generation when the error rate is low.

Furthermore, the media multiplexing rate in the multiplexing part 5 is changed according to the error rate; when the error rate is high, the media packet of video is divided to reduce the rate of the media packet of video contained in one multiplexed stream so that errors will not occur intensively in one video packet--this provides increased error resistance.

## 25 EMBODIMENT 5

Fig. 17 is a diagram depicting the configuration of a system to which a bit stream converter according to a fifth embodiment (Embodiment 5) of the

present invention. A description will be given of a system that carries out moving image transmissions between a terminal A connected to an ISDN circuit A1 and a multimedia terminal B connected to a radio channel B1. Reference numeral 101 denotes a system converter for converting the video coding scheme mutually between the terminal A and the multimedia terminal B in the case where the former is the transmitting side and the latter the receiving side. Reference numeral 102 denotes a bit stream (a second bit stream) coded by H. 263 as a multimedia multiplexed stream packetized by an ISDN media multiplexing system such as ITU-T. 221. Reference numeral 103 denotes a bit stream (a first bit stream) coded by MPEG-4 as a multimedia multiplexed stream packetized by a media multiplexing system for radio channel use such as ITU-T. 223. As depicted in Fig. 18, the coded bit stream 103 is composed of an up-link MPEG-4 coded bit stream 103a and a down-link MPEG-4 coded bit stream 103b.

Incidentally, there are some pieces of other media information such as those of audio information accompanying moving images, respectively, these pieces of multimedia information are sent as one multiplexed stream to the channel. The following description will be given, for brevity sake, on the assumption that only pieces of coded video data are multiplexed by a media multiplexing system predetermined for each channel.

Fig. 18 illustrate in block form the internal configuration of the bit stream converter according to this embodiment, and Fig. 19 illustrates in block form the internal configuration of a channel quality monitoring part of the bit stream converter. Reference numeral 104 denotes a channel quality monitoring part (channel quality monitoring means) for monitoring the error status of the radio channel B1. The channel quality monitoring part 104 comprises: media demultiplexer means which receives the down-link MPEG-

4 coded bit stream 103b and demultiplexes it into media multiplexing packets (AL-PDU) which are to be converted; and bit error detecting means 104b which makes a check to see if bit errors are contained in a CRC field added to each packet separated by the media demultiplexing means 104a (which CRC  
 5 field is a fixed-length bit generated using data in the packet), counts the number of bit errors, calculates an average error rate at predetermine time intervals based on the error count value, and outputs it as an internal signal 108 to an error resistance syntax determining part 105.

Reference numeral 105 denotes an error resistance syntax determining  
 10 part, which: receives from the channel quality monitoring part 104 the internal signal 108 indicating the error rate; selects, in accordance with the error rate, MPEG-4 error resistance syntaxes to be standardized, one by one or in combination of them, by the ISO described below; reads therein from an external device an error resistance syntax changing period 107 which  
 15 determines the timing for changing the error resistance syntax according to the channel condition; and, based on the error resistance syntax changing period 107, outputs the error resistance syntax selection result 109 to a syntax converting part 106.

By the error resistance syntax changing period 107 set to one frame,  
 20 for example, it is possible to change the error resistance syntax every other frame. If the changing period 107 is set to 15 frames, the error resistance syntax can be changed at intervals of 15 frames. While in the above the error resistance syntax changing period 17 has been described to be set to an arbitrary number of frame from the outside, the system converter 101 can also  
 25 be configured as to select a syntax in disregard of the set value when the given period is not appropriate, for example, at the instant the error rate undergoes an extreme variation.

Reference numeral 106 denotes a syntax converting part, which, when supplied with the error resistance syntax selection result 109 from the error resistance syntax determining part 105, converts the syntax of the down-link H. 263 coded bit stream 103b to the syntax of the up-link MPEG-4 coded bit stream 103a. The conversion of syntax in the syntax converting part 106 corresponds to processing by which: a bit stream encoded by the H. 263 coding scheme is once analyzed in a coded data area; the bits are converted into a bit string based on the definition of the MPEG-4 syntax; and, based on the selected error resistance syntax, the bit stream is used to reconstruct the syntax of the MPEG-4 video coded bit stream. This syntax conversion involves various processes, but they falls outside the scope of the present invention.

Next, a description will be given of four MPEG-4 error resistance syntaxes to be standardized by ISO. In MPEG-4 the following four error resistance syntaxes are supported at the level of the video coded bit stream syntax.

#### (1) Video Packet Structure (first error resistance syntax)

Fig. 20 is a diagrammatic showing of the MPEG-4 video packet structure. As shown, the video packet structure is a data unit which is composed of a video packet header and macro block data contained in a bit stream that immediately follows the video packet header and continues to a point immediately preceding the next video packet header. The video packet header, which begins with the resynchronization marker (a unique word for resynchronization), can be inserted in the bit stream at an arbitrary position. Hence, by inserting resynchronization markers in the bit stream at equal bit length intervals, it is possible to facilitate the detection of their positions and suppress deterioration of the picture quality in an area of an abrupt motion.

## (2) HEC Field (fourth error resistance syntax)

This is a video packet multiplexing scheme which optionally inserts important information of the VOP header in the video packet header. With this scheme, it is possible, for instance, to continue decoding through the use of information in an HEC field even if the reliability of the decoding status of a high order layer such as the VOP header is low. Incidentally, VOP is an abbreviation for “video object plane,” which is adapted so that a video sequence can be encoded for each object of an arbitrary form (such as a man or background); VOP corresponds to the frame or picture which is a time sampling unit of the conventional image encoding scheme. The frame and the picture are each regarded as special VOP which is rectangular and does not change its size with time.

## (3) Data Partitioning (second error resistance syntax)

Fig. 21 is a diagrammatic representation of an MPEG-4 data partitioning syntax. This syntax is one that partitions the macro block data contained in the video packet into data of major importance (such as the motion vector) and data of minor importance (such as DCT coefficient data) and encodes them separately. As depicted in Fig. 21, the unique word is interposed between the data of major importance and the data of minor importance, by which the boundary between the two data areas can be detected. This makes it possible for the decoding side to effectively perform error concealment by using the data of major importance when an error is detected in the data of minor importance.

## (4) Reversible VLC (third error resistance syntax)

This syntax is one that is predicated on the data partitioning syntax and that encodes a variable-length code of DCT coefficient data multiplexed on the data of minor importance through the use of uniquely reversible VLC.

In the data area encoded using the reversible VLC, even if an error is detected, error-free data can normally be decoded by decoding the data area in the opposite direction.

These error resistance syntaxes can be flexibly selected in combination, except mutually dependent relationships between the syntaxes (1) and (3) or between (3) and (4). As the result of the syntax selection, a syntax path changes accordingly. In general, an increase in the number of such functions provides increased error resistance. However, this entails such disadvantages as follows: in the case of (1), a decrease in the transmission efficiency for the video packet header; in the case of (2), a decrease in the transmission efficiency for the HEC field; in the case of (3), an increase in the number of memories needed during decoding; and in the case of (4), decreased transmission efficiency by the reversible VLC as compared with the transmission efficiency by normal VLC.

Accordingly, the system converter 101 in this embodiment provides a bit stream syntax that increases the transmission efficiency when the error rate of the radio channel B1 is low and, when the error rate is high, becomes robust against errors even at the price of the transmission efficiency.

Next, the operation of the converter 101 will be described below.

Fig. 22 is a flowchart showing the operation procedure of the bit stream converter according to Embodiment 5 of the present invention.

In the first place, the system converter 101 sets in the error resistance syntax determining part 105 the error resistance syntax changing period 107 read from an external device (step ST101). Then, the channel quality monitoring part 104: receives the down-link MPEG-4 coded bit stream 103b via the radio channel B1; separates it into packets (AL-PDU) for media multiplexing to be converted; makes a check to see if a bit error is contained



in the CRC field added to each packet, and counts the number of bit errors; calculates an average error rate at predetermined time intervals based on the bit error count value; and outputs the average error rate as the internal signal 108 to the error resistance syntax determining part 105 (step ST102).

5           Next, the error resistance syntax determining part 105 inserts video packets as the minimum error resistance syntax required to cope with the condition of the radio channel B1 (step ST103). When the radio channel B1 is in good condition, there are cases where the error resistance syntax determining part 105 does not insert any video packets or reduces the number of video packets to be inserted. The coded moving image bit stream 102 (H. 236 coded bit stream) can be added with one resynchronization marker for each unit GOB (Group OF Block). Since GOB is always fixedly positioned on the video plane, it is impossible to insert the resynchronization marker in accordance with the property or error characteristic of the image. The video packet insertion may also be done by converting the resynchronization marker of GOB to the resynchronization marker of the video packet without changing its position.

Next, at the instant of the syntax change timing based on the error resistance syntax changing period 107, the error resistance syntax determining part 105 makes a check to determine whether the channel error rate is lower than a predetermined threshold value EL1 (step ST104). In the case of “YES,” that is, if the error rate is lower than the threshold value EL1, the H. 263 MPEG-4 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103 without using any error resistance syntaxes (step ST108). In the case of “NO,” that is, when the channel error rate is higher than the predetermined threshold value EL1, the data partitioning syntax is used (step ST105) and the H. 263 coded bit stream 102 is converted to the MPEG-4

coded bit stream 103.

Next, at the instant the syntax change timing based on the error resistance syntax changing period 107, a check is made to see if the channel error rate is lower than a predetermined threshold value EL2 (which is smaller than EL1) (step ST106). In the case of “YES,” that is, when the error rate is lower than the threshold value EL2, the H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103 without using other error resistance syntaxes (step ST108). On the other hand, in the case of “NO,” that is, when the channel error rate is higher than the threshold value EL2, the error resistance needs to be increased; then, the reversible VLC is used (step ST107) and H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103 (step ST108).

Another method of operation of the system converter 101 will be described below.

Fig. 23 is a flowchart showing another operation procedure of the bit stream converter according to Embodiment 5. Steps ST101 to ST104 are the same as those in Fig. 21, and hence no description will be given of them. In the case of “NO” in step ST104, that is, when the channel error rate is higher than the predetermined threshold value EL1, the syntax of the HEC field is adopted (step ST109) and the H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103.

Next, at the instant the syntax change timing based on the error resistance syntax changing period 107, a check is made to determine whether the channel error rate is lower than the predetermined threshold value EL2 (step ST110). In the case of “YES,” that is, when the channel error rate is lower than the threshold value EL2, the H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103 without using any other error

resistance syntaxes (step ST108). On the other hand, in the case of “NO” in step ST 110, that is the channel error rate is higher than the threshold value EL2, the error resistance needs to be increased; then, the data partitioning syntax is adopted (step ST111) and the H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103.

Following this, at the instant of the syntax change timing based on the error resistance syntax changing period 107, a check is made again to determine whether the channel error rate is lower than a predetermined threshold value EL3 (which is larger than EL2) (step ST112). In the case of “YES,” that is, when the channel error rate is lower than the threshold value EL3, the H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103 without using any other error resistance syntaxes (step ST108). On the other hand, in the case of “NO” in step ST112, that is, when the channel error rate is higher than the threshold value EL3, the error resistance needs to be increased; then, the reversible VLC is adopted (step ST113) and the H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103 (step ST108).

Still another method of operation of the system converter 101 will be described below.

Fig. 24 is a flowchart showing another operation procedure of the bit stream converter according to Embodiment 5. The steps ST101, 102 and 108 are the same as those in Fig. 22, and hence no description will be given of them. At the instant of the syntax change timing based on the error resistance syntax changing period 107, the error resistance syntax determining part 105 makes a check to see if the channel error rate is higher than a predetermined threshold value EL (step ST120; in the case of “YES,” that is, when the error rate is higher than the threshold value, all the error resistance

syntaxes (step ST121) are selected and the H. 263 coded bit string 102 is converted to the MPEG-4 coded bit stream 103 (step ST108). On the other hand, in the case of "NO" in step ST120, that is, when the channel error rate is lower than the predetermined threshold value EL, the H. 263 coded bit stream 102 is converted to the MPEG-4 coded bit stream 103 (step ST108) without using any error resistance syntaxes.

As described above, this embodiment is provided with the channel quality monitoring part 104 which implements channel quality monitoring means and the error resistance syntax determining part 105 which implements syntax rule decision means; hence it is possible to provide an efficient bit stream according to the channel condition while achieving a balance between the degree of error resistance of the moving image coded bit stream to be fed over a channel of high error rate and the transmission efficiency as a whole. This embodiment is particularly effective in implementing moving picture communications between the communication terminal connected to the radio channel which supports the MPEG-4 video and the communication terminal connected to the ISDN or existing public network which supports the ITU-TH. 263 video.

Incidentally, the system converter 101 according to this embodiment performs conversion for the moving image coded bit stream, and hence it produces the same effects as described above even when the terminal A is an H. 324 terminal assumed to be connected to an ordinary analog public network or ISDN circuit based on H. 263, or an H. 323 terminal assumed to be connected to the Internet.

25

## EMBODIMENT 6

Fig. 26 is a block diagram illustrating the internal configuration of a

bit stream converter according to a sixth embodiment (Embodiment 6) of the present invention. The same parts as those in Embodiment 5 are identified by the same reference numerals and no description will be repeated. While Embodiment 5 is adapted to detect the channel error rate of the system converter 101 itself by the channel quality monitoring part 104, the error rate  
5 converter 101 itself by the channel quality monitoring part 104, the error rate may also be entered from the outside of the system converter 101. This could be implemented using, for example, a system configuration in which the network itself monitors the quality of service (QoS). In such an instance, it is possible to employ a device configuration which dispenses with the  
10 channel quality monitoring part 104 in Fig. 18 but instead enters the error rate 123 from the outside as in a system converter 122 of Fig. 25.

As described above, this embodiment is adapted to perform processing based on the error rate 123 which is input from the outside; hence, the same effect as is obtainable with Embodiment 5 can be produced without  
15 monitoring the channel quality by the converter itself.

## EMBPDIMENT 7

Fig. 26 is a block diagram illustrating a bit stream converter according to a seventh embodiment (Embodiment 7) of the present invention. This  
20 embodiment will be described in connection with a system converter which is contained in a media server which stores multimedia contents and delivers the contents in response to a request. Reference numeral 125 denotes contents stored as a video coded bit stream in a storage 126. Assume that the contents include bit streams encoded offline by an extremely high-quality  
25 processing system, or bit streams created on the premise that they are transmitted over a high-quality channel virtually insusceptible to errors. That is, assume that the contents 125 contains no particular syntax for error

resistance. Reference numeral 127 denotes a media server provided with a system converter 124 which converts the contents 125 to those 128 for transmission over the radio channel B1.

Next, the operation of this embodiment will be described below.

5 Fig. 27 illustrates in block form the internal configuration of the system converter 124 that constitutes the bit stream converter 127 according to this embodiment. The parts corresponding to those in Embodiment 5 are identified by the same reference numerals and no description will be repeated. In the case where the video encoding schemes for the contents 125 and 128 are H. 263 and MPEG-4, respectively, the same functional block can be used for the syntax converting part 106 and the error resistance syntax determining part 106. The error resistance syntax determining part 105 selects the error resistance syntax of the video coded bit stream of the contents 128 in accordance with the error rate 123 provided from the outside. The system converter may also be configured to select the error resistance syntax as described previously with reference to Figs. 22 and 23. Further, it may also be configured to adopt the alternative of selecting or not selecting the error resistance syntax.

20 It is possible to handle, as a special case, that the contents 125 and 128 are both MPEG-4 video coded bit streams. Let it be assumed here that the MPEG-4 video coded bit stream of the contents 125 has been created offline for storage use and encoded without using any error resistance syntax. In this case, since both the input and the output are MPEG-4 video coded bit streams, the syntax converting part 106 does not carry out the syntax conversion in terms of the coding scheme, but only adds selected error resistance syntaxes.

In such instance, too, the error resistance syntaxes can selectively be

added, and hence contents generated primarily for a different purpose can be sent over a channel of a different quality. Incidentally, the error rate 123 for judging the selection of the error resistance syntax need not always be a signal accurately indicating the channel condition, and its value may also be  
 5 designated according to a request from the server or user.

As described above, according to this embodiment, since the media server 127 connected to the radio channel B1 is equipped with the system converter 124, even video contents which are intended for storage use and do not much support the error resistance syntax can easily be converted to a  
 10 error-resistant bit stream when they are sent over the radio channel.

### **EMBPDIMENT 8**

Fig. 28 illustrates in block form the internal configuration of a system converter according to an eighth embodiment (Embodiment 8). The parts  
 15 corresponding to those in Embodiment 5 are identified by the same reference numerals and no description will be repeated. This embodiment is directed to a system converter 110 which converts MPEG-4 on the radio channel B1 side to H. 263 on the ISDN side in the same system as shown in Fig. 17. In this embodiment the terminal B is the transmitting terminal and the terminal  
 20 A is the receiving terminal. Reference numeral 111 denotes an MPEG-4 syntax analysis part (syntax analysis means) which: analyzes the input MPEG-4 bit stream 103 according to the MPEG-4 standard; makes a check for a decoding error during analysis; if a decoding error is detected, outputs an error detection signal 115; and separates the MPEG-4 coded bit stream into  
 25 individual pieces of coded data 116 and outputs them.

Reference numeral 112 denotes a switch which: upon receiving the error detection signal 115 from the MPEG-4 syntax analysis part 111, outputs

the input coded data 116 from the analysis part 111 to an error data converting part 113; and when not supplied with the error detection signal 115, provides the coded data 116 to an MPEG-4 syntax constituting part 114. Reference numeral 113 denotes an error data converting part in which a normally unanalyzable bit stream portion of the input coded data 116 is converted, with a minimum deterioration of picture quality, to a substitute value which does not cause an analysis error on the H. 263 syntax after being converted. Reference numeral 114 denotes an H. 263 syntax constituting part (coded data converting means) in which the coded data 117 provided from the error data converting part 113 or the coded data 116 provided via the switch 112 from the MPEG-4 syntax analysis part 111 is reconstructed as the H. 263 coded bit stream 102.

Next, the operation of this embodiment will be described below.

Fig. 29 is a flowchart showing the operation procedure of the bit stream converter according to this embodiment.

In the first place, the system converter 110 analyzes the input MPEG-4 coded bit stream 103 according to the MPEG-4 standard (step ST112), and makes a check for a decoding error in the process of analysis (step ST113). In the case of "YES," that is, when a decoding error is detected, the system converter 110 outputs the error detection signal, and at the same time separates the MPEG-4 coded bit stream 103 to individual pieces of coded data 116 and outputs them. On the other hand, in the case of "NO" in step ST113, that is, when no decoding error is detected, the system converter 110 goes to step ST115.

Then, the error data converting part 113 converts a normally unanalyzable bit stream portion of the input coded data 116, with a minimum deterioration of picture quality, to an alternate value (data for concealment)



which does not cause an analysis error on the H. 263 syntax after being converted (step ST114). The alternate value thus obtained is provided to the H. 263 syntax constituting part 114. In the H. 263 syntax constituting part 114, the coded data 117 from the error data converting part 113, or the coded data 116 provided via the switch 112 from the MPEG-4 syntax analysis part 111 is reconstructed as the H. 263 coded bit stream (step ST115).

Now, a description will be given of a method of converting the normally unanalyzable bit stream portion, while keeping the deterioration of the picture quality to a minimum, to an alternate value that does not cause an analysis error on the H. 263 syntax.

For example, consider the case where an error occurs in the DSCT coefficient area of a seventh one of ten macro blocks contained in a certain MPEG-4 video packet. Let it be assumed, however, that decoding is restored normal in the next video packet. In this instance, all the pieces of data from the DCT coefficient area of the seventh macro block to eighth, ninth and tenth macro blocks of the said certain video packet cannot be decoded normally.

Accordingly, it is unknown what value should be used for conversion to the H. 263 syntax; for the MPEG-4 coded bit stream area impossible of normal decoding, the system converter 110 of this embodiment sets an alternate value which will minimize the deterioration of picture quality, and uses it as analysis data for conversion to the H. 263 syntax.

As for such an alternate value, if there is the possibility of the unanalyzable macro blocks being subjected to inter-frame motion compensation predictive coding, the value of the motion vector is set to zero and the values of the DCT coefficients are also set to zeroes. It is already known whether the VOP containing the current macro blocks has been coded

by the inter-frame motion compensation predictive coding, since the VOP header has already been analyzed.

This is a situation in which the reference image to be used by the VOP for prediction is stored in a frame memory. In the case where no motion is made between VOPs by setting the motion vector to zero, a sufficiently reliable predictive image can be obtained, and by setting the DCT coefficients to zeroes, it is possible to reconstruct the predictive image intact as the H. 263 coded bit stream without containing therein extra prediction residual components. Further, in the analysis of the syntax using the MPEG-4 data partitioning scheme, when an error is detected after the unique word shown in Fig. 21, it is possible to choose a procedure by which the motion vector data analyzed before the unique word is used intact and only the DCT coefficients are set to zeroes.

In this case, if the motion vector analyzed before the unique word is reliable data, a highly reliable predictive image can be obtained with an extremely high degree of accuracy and it can be reconstructed as the H. 263 coded bit stream. Incidentally, by setting the DCT coefficients to zeroes, the prediction residual components are ignored. In a coded bit stream encoded at a low bit rate, however, since the dynamic range of the DCT coefficients is inherently so narrow that they tend to be distributed in the neighborhood of zero, the decoded image can sufficiently be approximated only with the predictive image in many cases.

As described above, according to this embodiment, even if a bit error gets mixed in the MPEG-4 coded bit stream that is sent over the radio channel, it is possible to achieve the conversion to the H. 263 syntax while keeping the influence of the error to a minimum. Accordingly, stable decoding can be done even in the case where the channel quality is high and an H. 263 decoder

of low resistance to errors is used.

Incidentally, in this embodiment the 320 terminal is used as the terminal A, but since the system converter according to this embodiment performs conversion of the moving image coded bit stream, it produces the same effects as described above even when the terminal A is an H. 324 terminal assumed to be connected to an ordinary analog public network or ISDN circuit based on H. 263, or an H. 323 terminal assumed to be connected to the Internet. This embodiment is particularly effective in implementing moving picture communications between the communication terminal connected to the radio channel which supports the MPEG-4 video and the communication terminal connected to the ISDN or existing public network which supports the ITU-TH. 263 video.

## EMBODIMENT 9

Fig. 30 illustrates in block form the internal configuration of system converter of a bit stream converting device according to a ninth embodiment (Embodiment 9) of the present invention. Reference numeral 111 denotes an MPEG-4 syntax analysis part which: analyzes the input down-link MPEG-4 bit stream 103 according to the MPEG-4 standard; makes a check for a decoding error during analysis; if a decoding error is detected, outputs an error detection signal 115; and separates the down-link MPEG-4 coded bit stream into individual pieces of coded data 116 and outputs them.

Reference numeral 112 denotes a switch which: upon receiving the error detection signal 115 from the MPEG-4 syntax analysis part 111, outputs the input coded data 116 from the analysis part 111 to an error data converting part 113; and when not supplied with the error detection signal 115, provides the coded data 116 to an MPEG-4 syntax constituting part 114. Reference

numeral 113 denotes an error data converting part in which a normally unanalyzable bit stream portion of the input coded data 116 is converted, with a minimum deterioration of picture quality, to a substitute value which does not cause an analysis error on the H. 263 syntax after being converted.

- 5 Reference numeral 114 denotes an H. 263 syntax constituting part in which the coded data 117 provided from the error data converting part 113 or the coded data 116 provided via the switch 112 from the MPEG-4 syntax analysis part 111 is reconstructed to the up-link H. 263 coded bit stream 102a.

- Reference numeral 104 denotes a channel quality monitoring part  
10 which: receives and demultiplexes the down-link MPEG-4 coded bit stream 103b into media multiplexing packets (AL-PDU) to be converted; makes a check to see if bit errors are contained in a CRC field added to each packet; counts the number of bit errors; calculates an average error rate at predetermine time intervals based on the error count value; and outputs it as  
15 an internal signal 108 to an error resistance syntax determining part 105.

- Reference numeral 105 denotes an error resistance syntax determining part, which: receives from the channel quality monitoring part 104 the internal signal 108 indicating the error rate; selects, in accordance with the error rate, MPEG-4 error resistance syntaxes one by one or in combination of them;  
20 reads therein from an external device an error resistance syntax changing period 107 which determines the timing for changing the error resistance syntax according to the channel condition; and, based on the error resistance syntax changing period 107, outputs the error resistance syntax selection result 109 to a syntax converting part 106b.

- 25 Reference numeral 106a denotes an H. 263 syntax analysis part (syntax analysis means) which analyzes the input down-link H. 263 coded bit stream 102b according to the H. 263 standard and separates the down-link

coded bit stream 102b into individual pieces of coded data 116. Reference numeral 106b denotes an MPEG-4 syntax constituting part (coded data converting mean), which, when supplied with the error resistance selection result 109 from the error resistance syntax determining part 105, converts the  
 5 coded data 116 from the H. 263 syntax analysis part 106a to the up-link MPEG-4 coded bit stream 103a.

Next, the operation of this embodiment will be described below.

A description will be given first of the procedure for converting the MPEG-4 coded bit stream to the H. 263 coded bit stream.

10 The MPEG-4 syntax analysis part 111 receives the down-link MPEG-4 coded bit stream 103b, then analyzes it according to the MPEG-4 standard, and makes a check to see if whether a decoding error is detected. If a decoding error is detected, the analysis part 111 outputs the error detection signal 115 and separates the down-link MPEG-4 coded bit stream 103b into  
 15 individual pieces of coded data 116.

Upon receiving the error detection signal 115 from the MPEG-4 syntax analysis part 111, the switch 112 provides the coded data 116 also fed from the MPEG-4 syntax analysis part 111 to the error data converting part 111. In the absence of the error detection signal 115, the switch 115  
 20 provides the coded data 116 to the H. 263 syntax constituting part 114. Then, the error data converting part 113 converts a normally unanalyzable bit stream portion of the input coded data 116, with a minimum deterioration of picture quality, to an alternate value which does not cause an analysis error on the H. 263 syntax after being converted. And, in the H. 263 syntax constituting  
 25 part 114, the coded data 117 from the error data converting part 113, or the coded data 116 provided via the switch 112 from the MPEG-4 syntax analysis part 111 is reconstructed as the up-link H. 263 coded bit stream 102a.

Next, a description will be given of the procedure for converting the H. 263 coded bit stream to the MPEG-4 coded bit stream.

The channel quality monitoring part 104: receives and demultiplexes the down-link MPEG-4 coded bit stream 103b into media multiplexing packets (AL-PDU) to be converted; makes a check to see if bit errors are contained in the CRC field added to each packet; counts the number of bit errors; calculates an average error rate at predetermine time intervals based on the error count value; and outputs it as the internal signal 108 to the error resistance syntax determining part 105.

Then, error resistance syntax determining part 105: receives from the channel quality monitoring part 104 the internal signal 108 indicating the error rate; selects, in accordance with the error rate, MPEG-4 error resistance syntaxes one by one or in combination of them; reads therein from an external device the error resistance syntax changing period 107 which determines the timing for changing the error resistance syntax according to the channel condition; and, based on the error resistance syntax changing period 107, outputs the error resistance syntax selection result 109 to the syntax converting part 106b.

And, the H. 263 syntax analysis part (syntax analysis means) 106a analyzes the input down-link H. 263 coded bit stream 102b according to the H. 263 standard and separates the down-link coded bit stream 102b into individual pieces of coded data 116. Next, when supplied with the error resistance selection result 109 from the error resistance syntax determining part 105, the MPEG-4 syntax constituting part 106b converts the coded data 116 from the H. 263 syntax analysis part 106a to the up-link MPEG-4 coded bit stream 103a.

As described above, according to this embodiment, conversions

concerning the error resistance syntax can efficiently made, in accordance with the error rate of the channel connected, between terminals of a two-way moving picture communication which support MPEG-4 or H. 263, such as visual telephone or teleconference. This embodiment is particularly effective in implementing moving picture communications between the communication terminal connected to the radio channel which supports the MPEG-4 video and the communication terminal connected to the ISDN or existing public network which supports the ITU-TH.263 video.

## 10 EMBODIMENT 10

Fig. 31 illustrates in block form a system converter of a bit stream converting device according to tenth embodiment (Embodiment 10) of the present invention. The parts corresponding to those in Embodiments 5 and 6 are identified by the same references and no description will be repeated. This embodiment is directed to a system converter 119 which converts MPEG-4 at the radio channel side to H. 283 at the ISDN side. In this case, the terminal B is the transmitting terminal and the terminal A is the receiving terminal in Fig. 17.

Reference numeral 120 denotes an error data generating part. For a normally unanalyzable bit stream area of the coded data 116 input thereto via the switch 112, the error data generating part 120 intentionally generates H. 263 data 121 containing an error so that data at the position of that area or in its vicinity is similarly detected as a decoding error at the terminal which receives the converted H. 263 bit stream.

Now, a description will be given of a concrete method for generating the data 121 in the error data generating part 120.

For example, consider the case where an error occurs in the DSCT

coefficient area of a seventh one of 10 macro blocks contained in a certain MPEG-4 video packet. Let it be assumed, however, that decoding is restored normal in the next video packet. In this instance, all the pieces of data from the DCT coefficient area of the seventh macro block to eighth,  
 5 ninth and tenth macro blocks of the said certain video packet cannot be decoded normally. Accordingly, it is unknown what value should be used for conversion to the H. 263 syntax.

For such an MPEG-4 coded bit stream area impossible of normal decoding, the system converting device 119 of this embodiment intentionally  
 10 generates H. 263 data containing an error so that data at the position of that area or in its vicinity is similarly detected as a decoding error at the terminal which receives the converted H. 263 bit stream.

In the case where the DCT coefficient area is unanalyzable, the error data generating part 120 intentionally generates a code word that is not  
 15 contained in a VLC table of DCT coefficients for H. 263, or a code word which contains 64 or more DCT coefficients (Since the discrete cosine transform is usually performed for each block composed of 8 by 8 pixels, decoding of 64 or more pieces of coefficient data is actually impossible under normal decoding condition.) and such a code word is provided to an H. 263  
 20 syntax constituting part 114.

Alternatively, when an error is detected in the motion vector area, the error data generating part 120 intentionally generates a motion vector that lies offscreen, or a code word that is not contained in the VLC table of DCT  
 25 coefficients for H. 263, and provides it to the H. 263 syntax constituting part 114.

Next, the operation of this embodiment will be described below.

Fig. 32 is a flowchart for explaining the operation procedure of the bit



stream converter according to this embodiment.

In the first place, the system converter 119 analyzes the input MPEG-4 coded bit stream 103 in the MPEG-4 syntax analysis part 111 according to the MPEG-4 standard (step ST116), and makes a check for a decoding error in the process of analysis (step ST117). In the case of “YES,” that is, when a decoding error is detected, the system converter 110 outputs the error detection signal 115, and at the same time separates the MPEG-4 coded bit stream 103 to individual pieces of coded data 116 and outputs them. On the other hand, in the case of “NO” in step ST117, that is, when no decoding error is detected, the system converter 110 goes to step ST119.

Then, for the normally unanalyzable bit stream are of the coded data 116 input via the switch 112, the error data generating part 120 intentionally generates and outputs the H. 263 data 121 containing an error so that data in the unanalyzable bit stream area or in its vicinity is similarly detected as a decoding error at the terminal receiving the converted H. 263 bit stream (step ST118). Then, the H. 263 syntax constituting part 114 reconstructs and output, as the H. 263 coded bit stream 102, the data 121 from the error data generating part 120 or the coded data 116 provided via the switch 112 from the MPEG-4 syntax analysis part (step ST119).

As described above, according to this embodiment, even if a bit error gets mixed in the MPEG-4 coded bit stream that is sent over the radio channel, it is possible to perform the syntax conversion to H. 263 while keeping the error condition unchanged. Accordingly, this embodiment eliminates the necessity of taking measures for the system converter 119 itself against errors, permits simplification of the device configuration and ensures maintaining the picture quality according to measures taken at the receiving side against errors.

Furthermore, high picture quality can be obtained at a receiving terminal with a high error resistance syntax, that is, at each receiving terminal the picture quality can be obtained corresponding to the error resistance syntax used.

5           The system converter 119 of this embodiment is also applicable to the conversion of the H. 263 bit stream to the MPEG-4 one. In this instance, the system converter 119 employs an H. 263 syntax analysis part and an MPEG-4 syntax constituting part in place of the MPEG-4 syntax analysis part 111 and the H. 263 syntax constituting part 114, respectively. As regards an error  
10 detected in the H. 263 syntax analysis part 111, the error data generating part 120 intentionally generates data so that the MPEG-4 receiving terminal detects an error at the same position as the above error detection or in its vicinity. The operation procedure is similar to that depicted in Fig. 32.

This embodiment uses the H. 320 terminal as the terminal A, but since  
15 the system converter 119 performs conversion for the moving image coded bit stream, it produces the same effects as described above even when the terminal A is an H. 324 terminal assumed to be connected to an ordinary analog public network or ISDN circuit using H. 263, or an H. 323 terminal assumed to be connected to the Internet. This embodiment is particularly  
20 effective in implementing moving picture communications between the communication terminal connected to the radio channel which supports the MPEG-4 video and the communication terminal connected to the ISDN or existing public network which supports the ITU-TH.263 video.

## 25   EFFECT OF THE INVENTION

As described above, the video packet generating method, the video decoding method, the media multiplexer, the media demultiplexer, the

multimedia communication system and the bit stream converter according to the present invention are suitable for use in a system which adds an error detecting codes to each packetized information area, performs error detection and concealment at the receiving side, and effectively converts the error resistance syntax according to the channel condition to provide increased error resistance for moving picture communications.

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## WHAT IS CLAIMED IS:

1. A video packet generating method which divides a coded video stream into plural video segments and generates packets each added with an error detecting code for each of said plural video segments, wherein:

said coded video stream is composed of compressed block coded data, and said block coded data is composed of plural kinds of data elements, said data elements of the same kind being arranged in succession over plural blocks; and

said coded video stream is divided, at the point of change in the kind of said data elements arranged in succession, into said packets each added with said error detecting code for each of said plural video segments.

2. A video decoding method which receives a coded video stream, together with an error detection result indicating whether an error is contained in a coded stream in each packet, and decodes said coded video stream, wherein:

said coded video stream is composed of plural pieces of compressed block coded data, said plural pieces of compressed block coded data are composed of plural kinds of data elements, said data elements of the same kind are arranged in succession over plural blocks, and said coded video stream is divided, at the point of change in the kind of said data elements arranged in succession, into said each packet, said each packet being added, for each of said divided video coded streams, with an error detecting code for obtaining said error detection result; and

upon detecting a decoding error at the time of receiving and decoding said coded video stream for said each packet, the position of said decoding

error in said coded video stream is decided based on an error detection result received and error concealment is selectively performed based on said decided position of said decoding error.

5           3. The video decoding method of claim 2, wherein: said plural kinds of data elements contain a data stream composed of motion vectors contained in plural blocks and a data stream composed of pieces of texture information contained in plural blocks; and, based on said error detection result received together with each data stream and the position of said decoding error  
10 detected in the decoding of said each data stream, it is decided whether to perform error concealment using decoded motion vectors or abandon said motion vectors and said texture data and perform error concealment.

          4. A video packet generating method which divides a coded video  
15 stream into plural video segments and generates packets each added with an error detecting code for each of said plural video segments, wherein:

          said coded video stream is composed of compressed block coded data, and for each block coded data of plural blocks, header information is coded which contains a unique code indicating the head of said each block coded  
20 data and its block number; and

          said coded video stream is divided into packets at the point of change between said header information and said compressed block coded data, said packets being added with said error detecting code for each of said plural video segments.

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          5. A video decoding method which receives a coded video stream, together with an error detection result indicating whether an error is contained

in a coded stream in each packet, and decodes said coded video stream, wherein:

said coded video stream is composed of plural pieces of compressed block coded data, and for each of said compressed block coded data of plural  
 5 blocks, header information is coded which contains a unique code indicating the head of said each block coded data and its block number, and said coded video stream is divided into packets at the point of change between said header information and said block coded data, said packets being added, for each of said plural video segments, with an error detecting code for obtaining  
 10 said error detection result; and

upon detecting a decoding error during decoding of said coded video stream received for each packet, the position of resynchronization is decided based on said unique code and said error detection result received together with coded data of said header information and resynchronization is  
 15 performed from the bit position of error detection to a unique code indicating the beginning of the next block coded data.

6. A media multiplexer in which packets of coded video streams generated by said video packet generating method of claim 1 and packets of  
 20 audio and data, generated by dividing coded audio and data streams, are multiplexed into one stream for output as a multiplex stream.

7. A media multiplexer wherein packets of coded video streams generated by said video packet generating method of claim 4 and packets of  
 25 audio and data, generated by dividing coded audio and data streams, are multiplexed into one stream for output as a multiplex stream.

8. A media demultiplexer wherein: said multiplex stream by said media multiplexer of claim 6 is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each of said packets is decoded to obtain an error detection  
5 result; and

said packets of coded video streams are decoded into video packets by said video decoding method of claim 2 based on said error detection result for each coded video stream concerned.

10 9. A media demultiplexer wherein: said multiplex stream by said media multiplexer of claim 6 is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each of said packets is decoded to obtain an error detection  
result; and

15 said packets of coded video streams are decoded into video packets by said video decoding method of claim 3 based on said error detection result for each coded video stream concerned.

20 10. A media demultiplexer wherein: said multiplex stream by said media multiplexer of claim 6 is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each of said packets is decoded to obtain an error detection  
result; and

25 said packets of coded video streams are decoded into video packets by said video decoding method of claim 5 based on said error detection result for each coded video stream concerned.

11. A media demultiplexer wherein: said multiplex stream by said media multiplexer of claim 7 is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each of said packets is decoded to obtain an error detection  
5 result; and

said packets of coded video streams are decoded into video packets by said video decoding method of claim 2 based on said error detection result for each coded video stream concerned.

10 12. A media demultiplexer wherein: said multiplex stream by said media multiplexer of claim 7 is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each of said packets is decoded to obtain an error detection  
result; and

15 said packets of coded video streams are decoded into video packets by said video decoding method of claim 3 based on said error detection result for each coded video stream concerned.

20 13. A media demultiplexer wherein: said multiplex stream by said media multiplexer of claim 7 is demultiplexed into packets of coded video streams and packets of coded audio and data streams, and an error detecting code contained in each of said packets is decoded to obtain an error detection  
result; and

25 said packets of coded video streams are decoded into video packets by said video decoding method of claim 5 based on said error detection result for each coded video stream concerned.



14. A multimedia communication system which is provided with: a transmitting device which: encodes various pieces of media information, such as audio, video and data, into coded streams; divides said coded streams into various media packets; multiplexes said various media packets, each added  
 5 with an error detecting code, into a stream; and transmits said multiplex stream over a communication line; and a receiving device which: receives said multiplex stream; demultiplexes said multiplex stream into said various media packets; and decodes said various media packets in media information decoding parts corresponding thereto; wherein:

10 said receiving device comprises means for decoding said error detecting code added for each of said various media packets; and

said transmitting device comprises:

means for monitoring the error condition of said communication line based on an error detected in said receiving device; and

15 means for generating video packets by dividing a video coded string in varying unit of division according to the error condition of said communication line.

15. A multimedia communication system which is provided with: a  
 20 transmitting device which: encodes various pieces of media information, such as audio, video and data, into coded streams; divides said coded streams into various media packets; multiplexes said various media packets, each added with an error detecting code, into a stream; and transmits said multiplex stream over a communication line; and a receiving device which: receives  
 25 said multiplex stream; demultiplexes said multiplex stream into said various media packets; and decodes said various media packets in media information decoding parts corresponding thereto; wherein:

said receiving device comprises means for decoding said error detecting code added for each of said various media packets; and

said transmitting device comprises:

means for monitoring the error condition of said communication line  
5 based on an error detected in said receiving device; and

means for multiplexing said media packets of audio, video and data into one stream while changing the ratios of audio, video and data in said multiplex stream according to the error condition of said communication line.

10 16. A bit stream converter which converts a first bit stream containing moving image coding information into a second bit stream, comprising:

syntax rule decision means for changing a rule of a coded bit stream syntax;

wherein a predetermined one of plural syntax rules is selected by said  
15 syntax rule decision means for said conversion.

17. The bit stream converter of claim 16, wherein said syntax rule decision means selects one of said plural syntax rules according to the quality of a communication line over which said second bit stream is transmitted.

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18. The bit stream converter of claim 17, which is provided with line quality monitoring means for detecting an error of an input bit stream, and wherein the line quality is obtained by said line quality monitoring means.

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19. The bit stream converter of claim 16, wherein said syntax rule decision means selects predetermined combination of syntaxes, on a one-by-one basis, from four kinds of MPEG-4 error resistance syntaxes which are

resynchronization data unit/header multiplex/data division/two-way  
decodable code words.

20. The bit stream converter of claim 17, wherein said syntax rule  
5 decision means selects predetermined combination of syntaxes, on a one-by-  
one basis, from four kinds of MPEG-4 error resistance syntaxes which are  
resynchronization data unit/header multiplex/data division/two-way  
decodable code words.

10 21. The bit stream converter of claim 18, wherein said syntax rule  
decision means selects predetermined combination of syntaxes, on a one-by-  
one basis, from four kinds of MPEG-4 error resistance syntaxes which are  
resynchronization data unit/header multiplex/data division/two-way  
decodable code words.

15 22. A bit stream converter which converts a first bit stream containing  
moving image coding information into a second bit stream, comprising:

syntax analysis means which analyzes said first bit stream according  
to a predetermined rule, separates said bit stream into coded data and detects  
20 an analysis error; and

coded data converting means which converts coded data, lost by said  
analysis error, into an error-free value on a syntax based on a moving image  
coding system at the destination of said second bit stream.

25 23. A bit stream converter which converts a first bit stream containing  
moving image coding information into a second bit stream, comprising:

syntax analysis means which analyzes said first bit stream according

to a predetermined rule, separates said bit stream into coded data and detects an analysis error; and

coded data converting means which coded data at the position of detection of said analysis error or in its vicinity into data recognizable as an analysis error on a syntax based on a moving image coding system at the  
5 destination of said second bit stream.

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## ABSTRACT

A moving image coded string is mapped with a set of information areas as one packet, and each packet is added with an error code and control information in a multiplexing part so that it is decided at the receiving side, from the result of error detection by decoding the error detection code, whether an error has occurred in each information area of the packet.

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FIG. 1 (PRIOR ART)

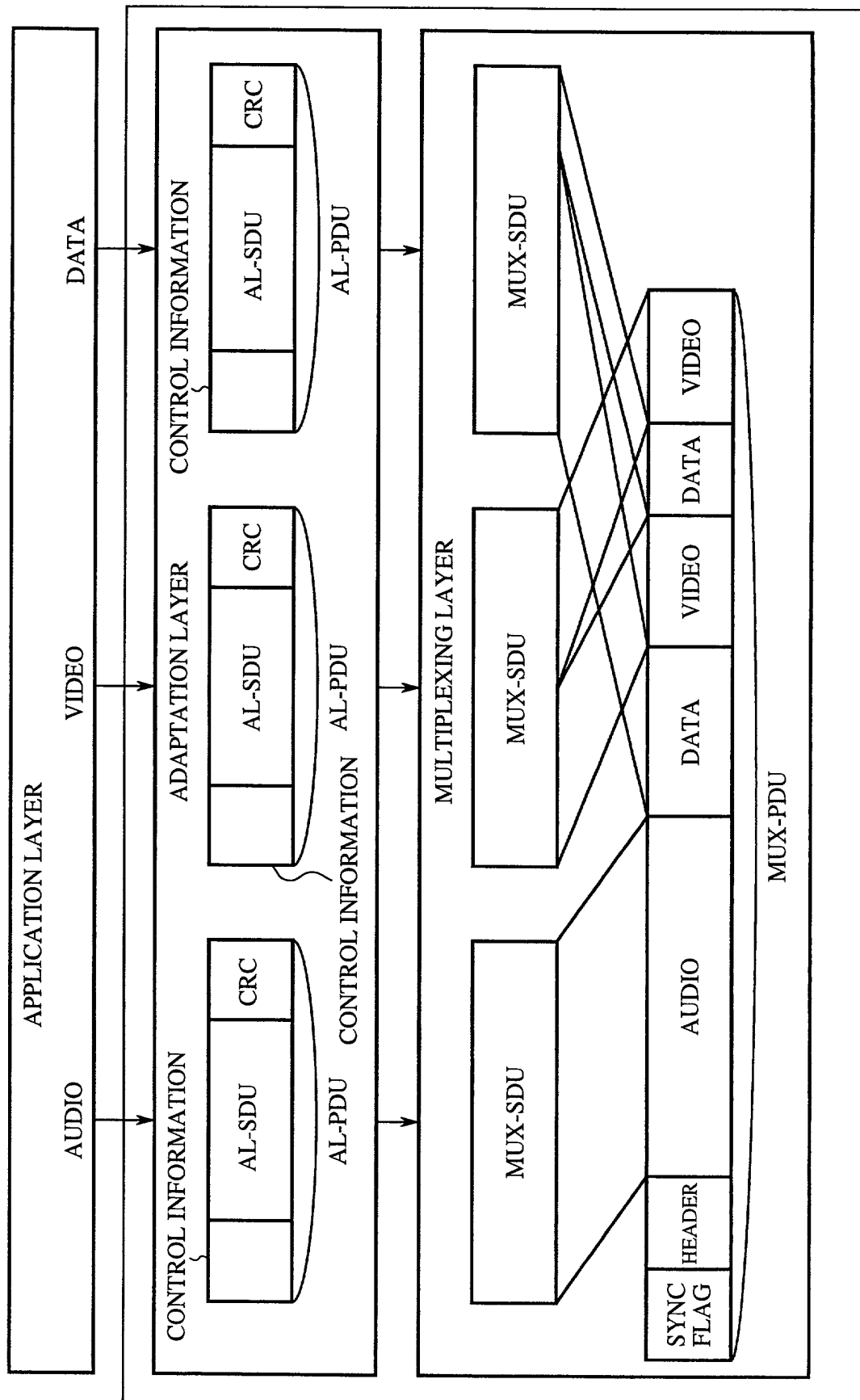


FIG.2

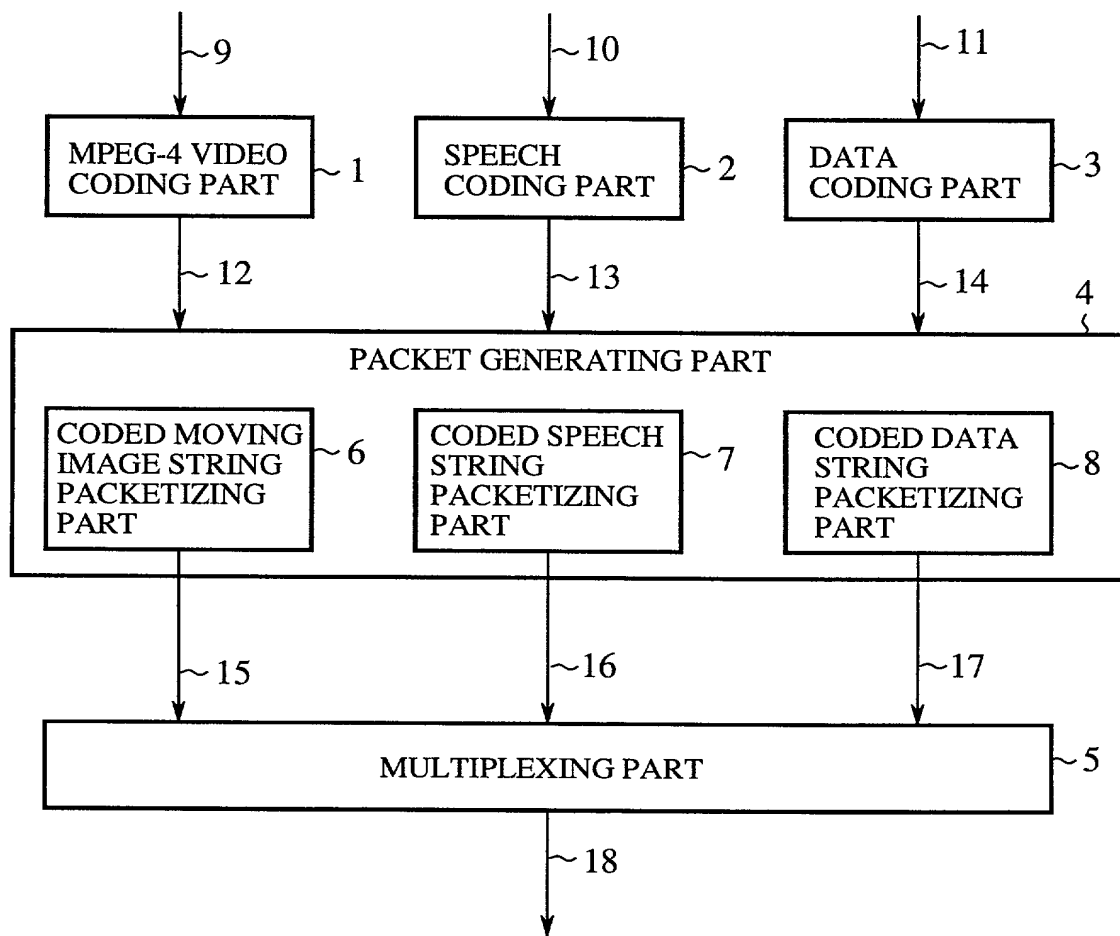


FIG.3A

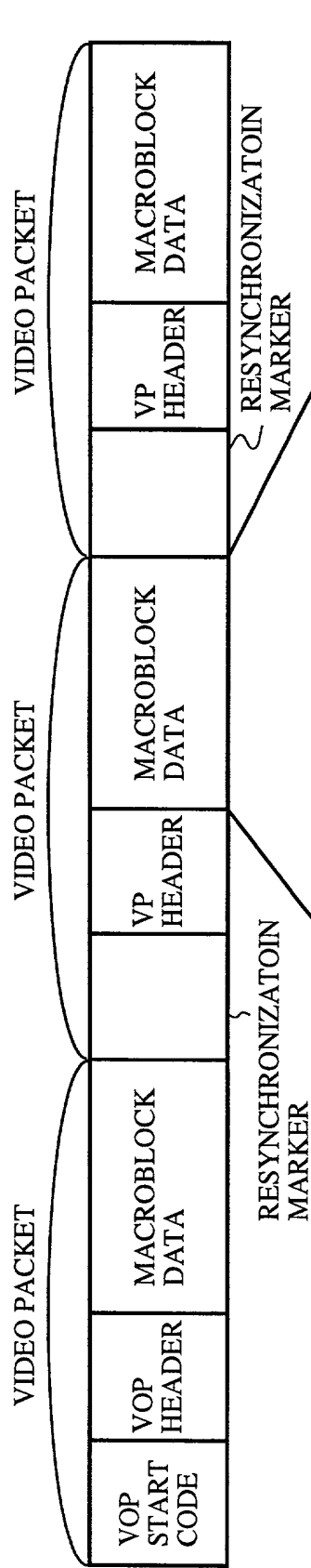


FIG.3B

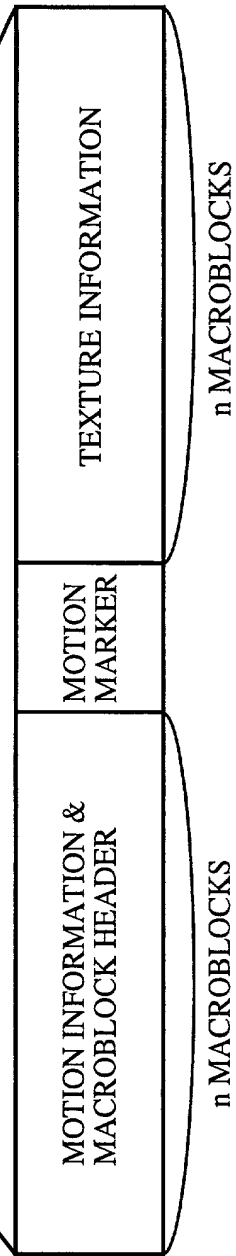




FIG. 4

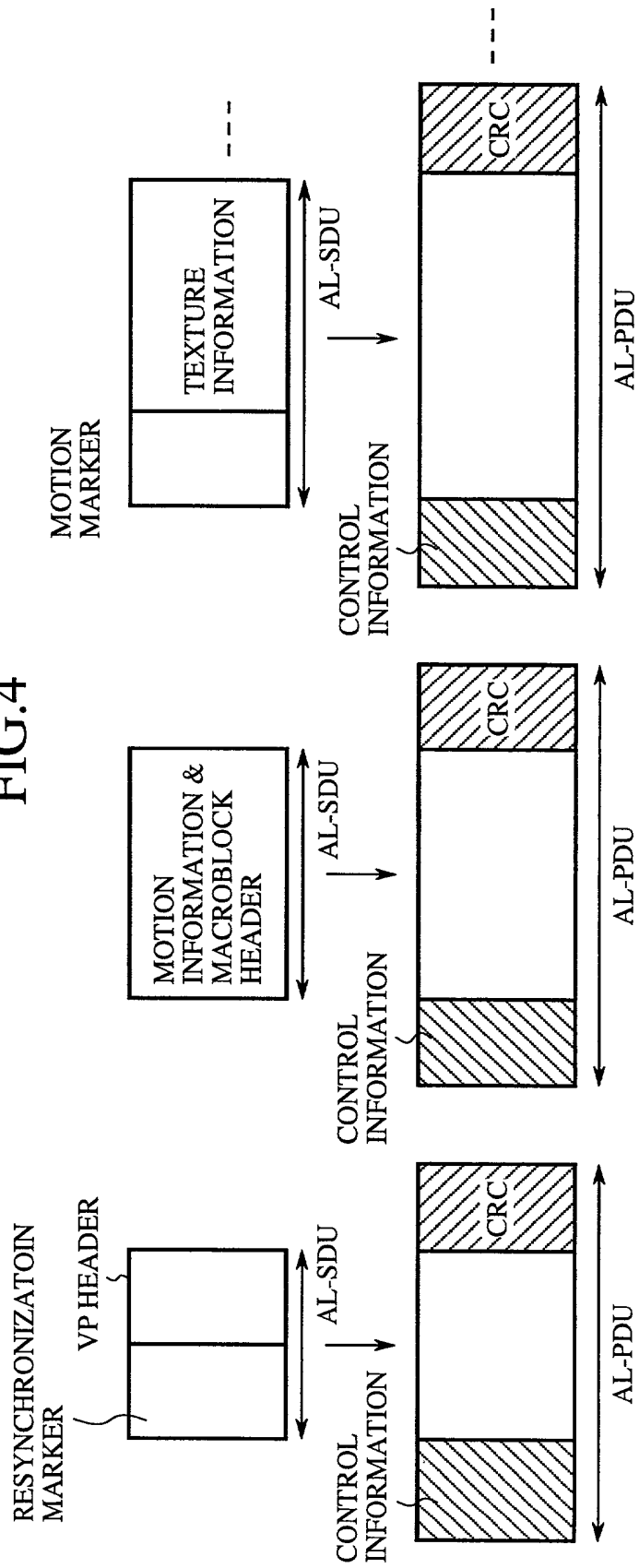
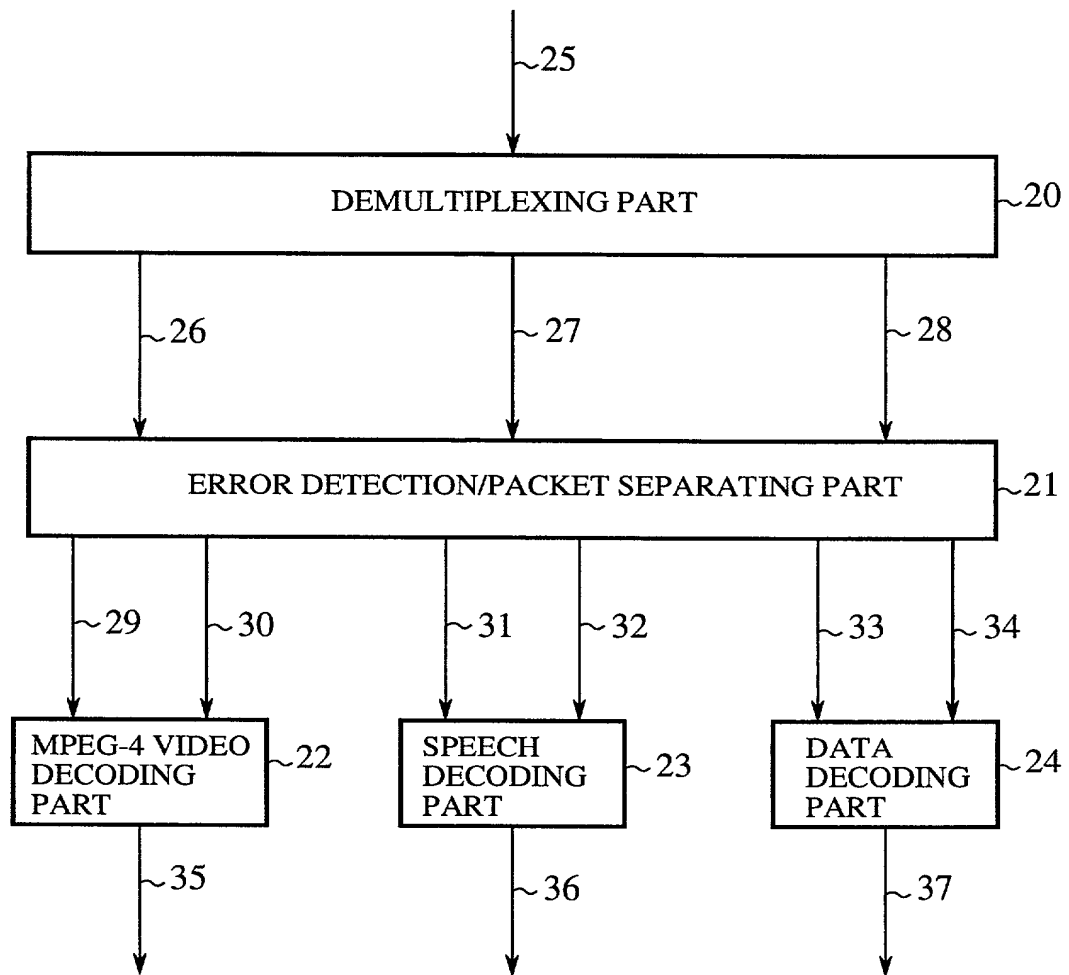


FIG.5



[illegible]

FIG.7

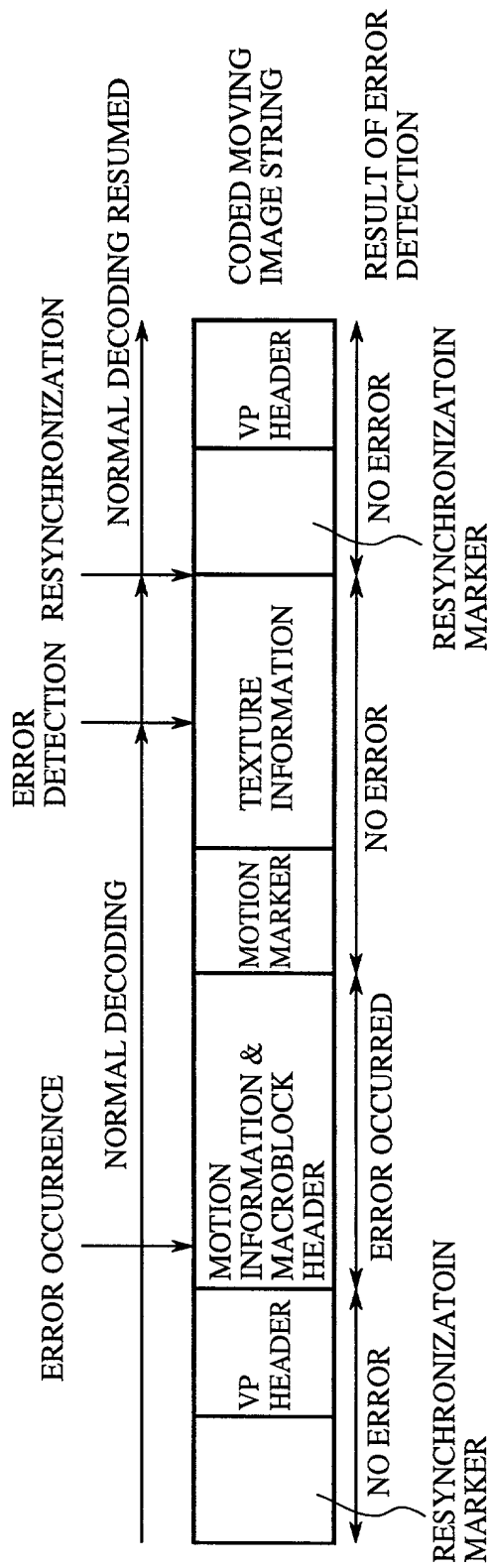


FIG.9

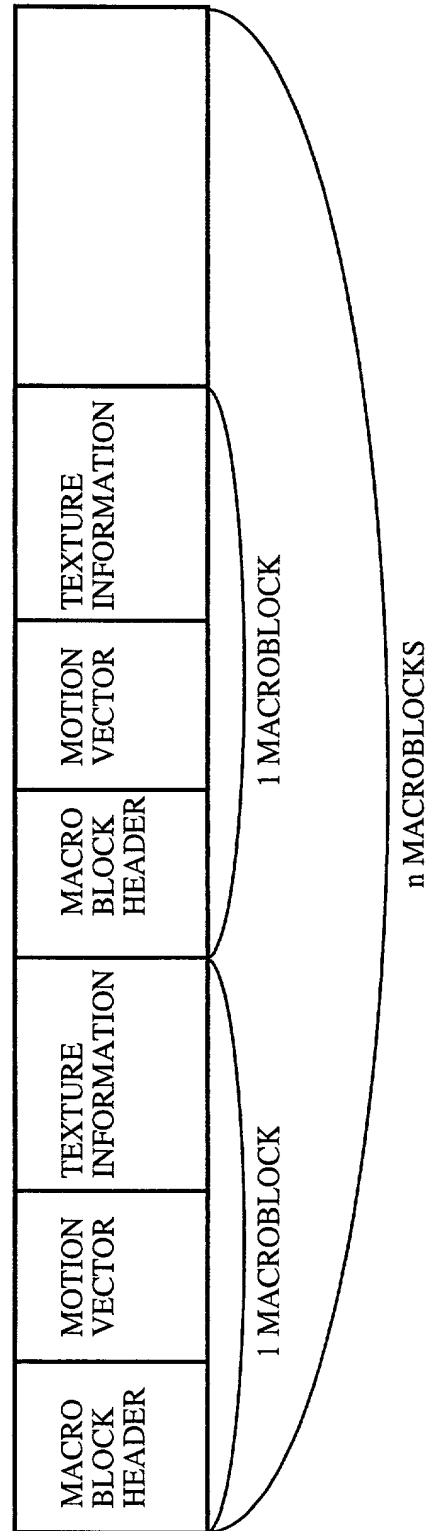




FIG. 10

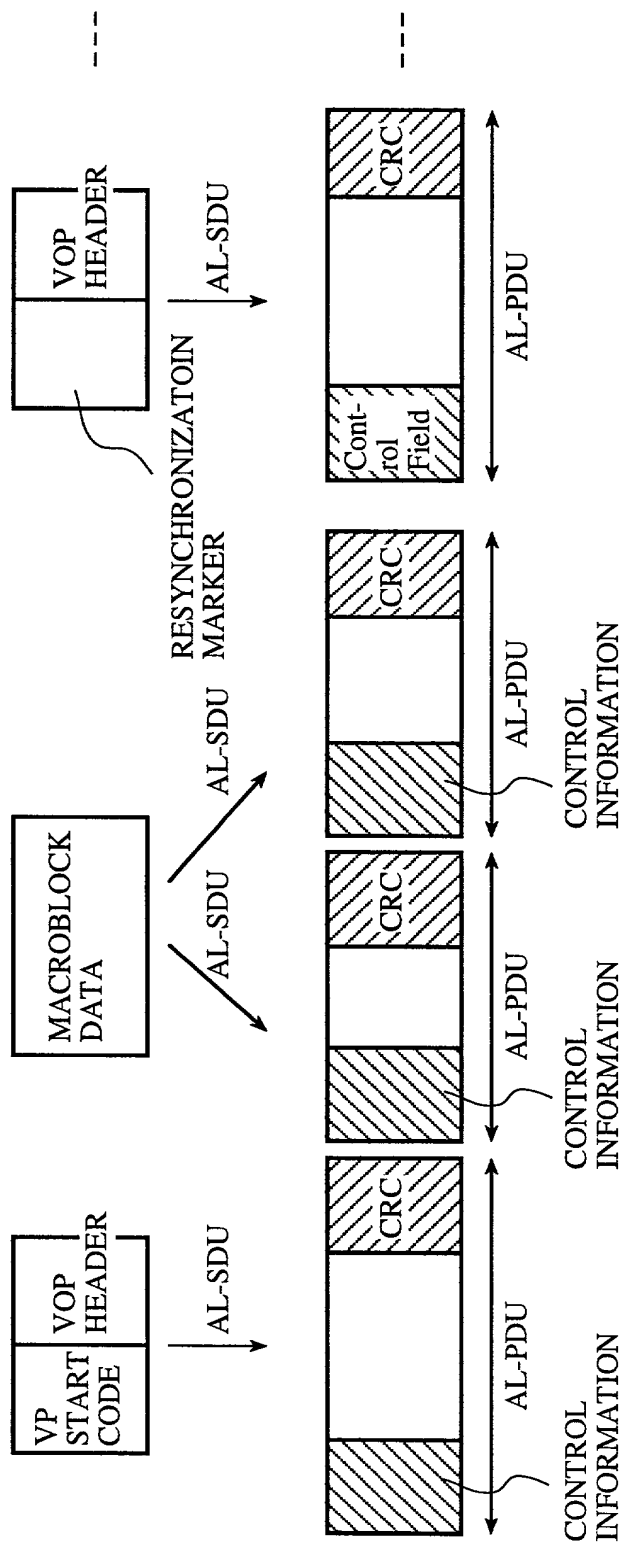


FIG. 12

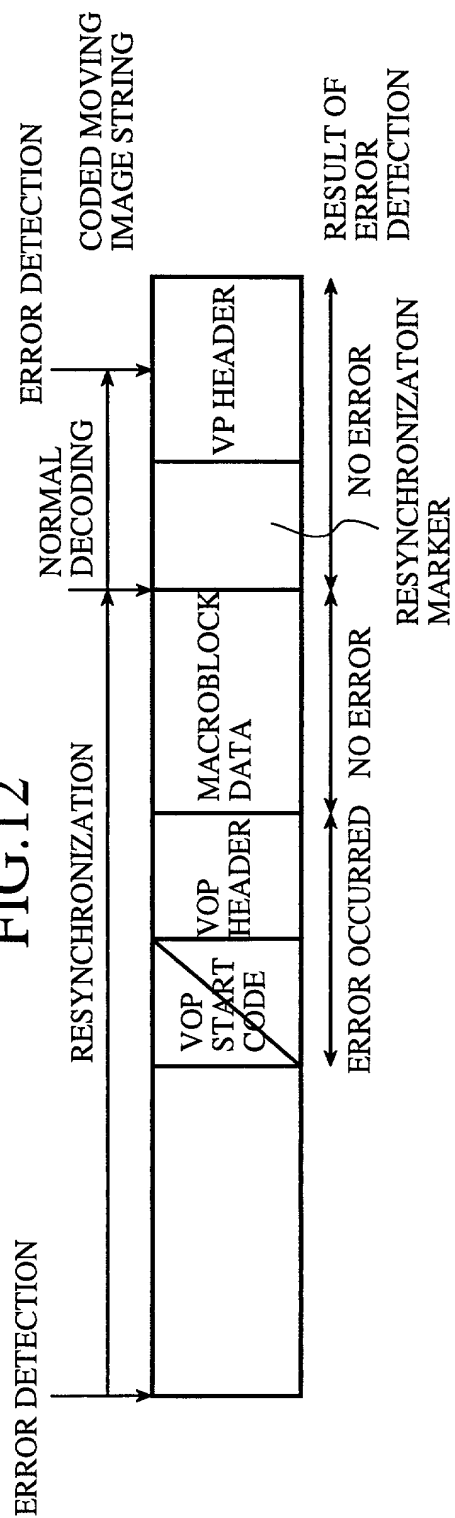


FIG.11

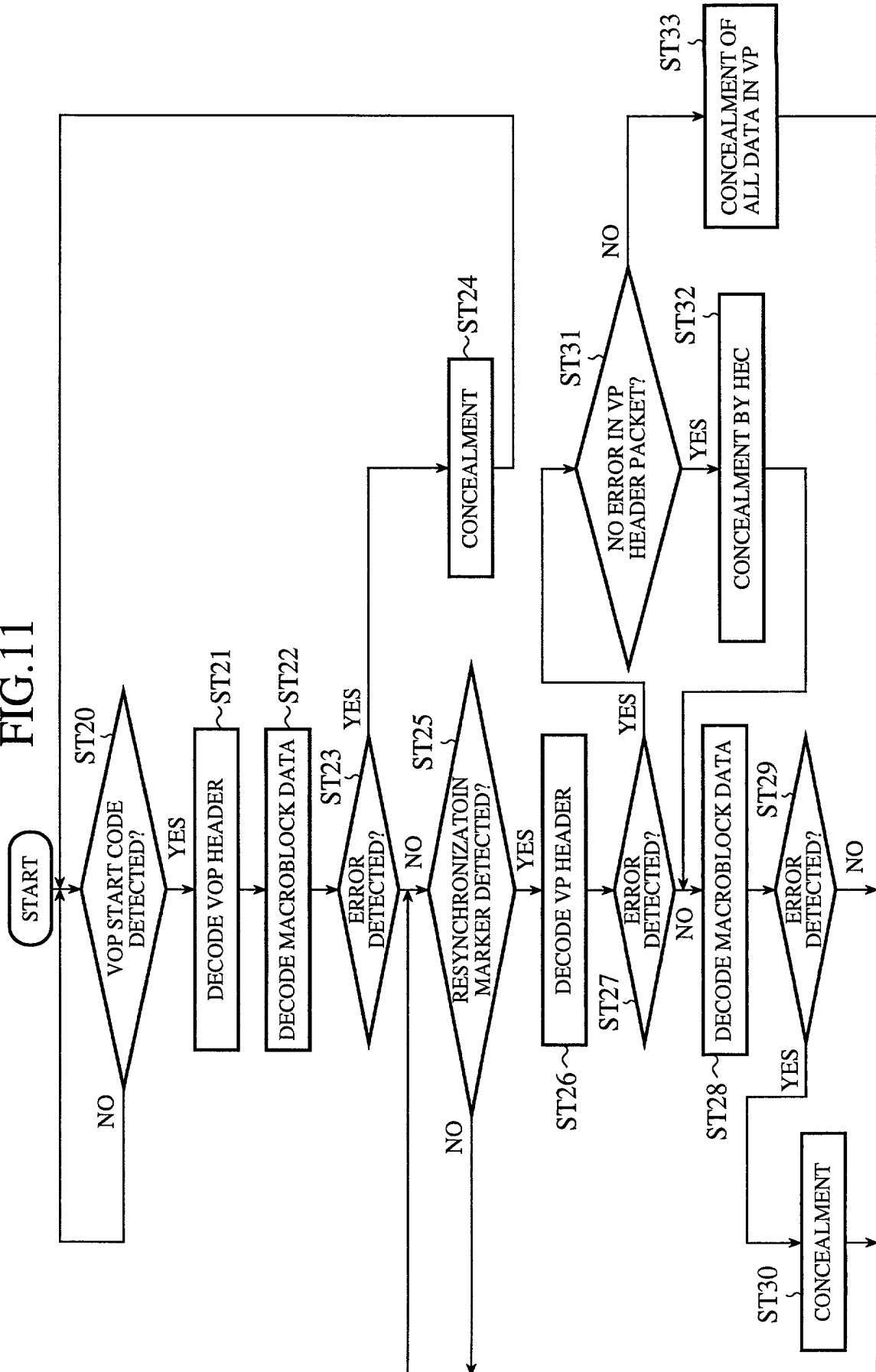


FIG.13A

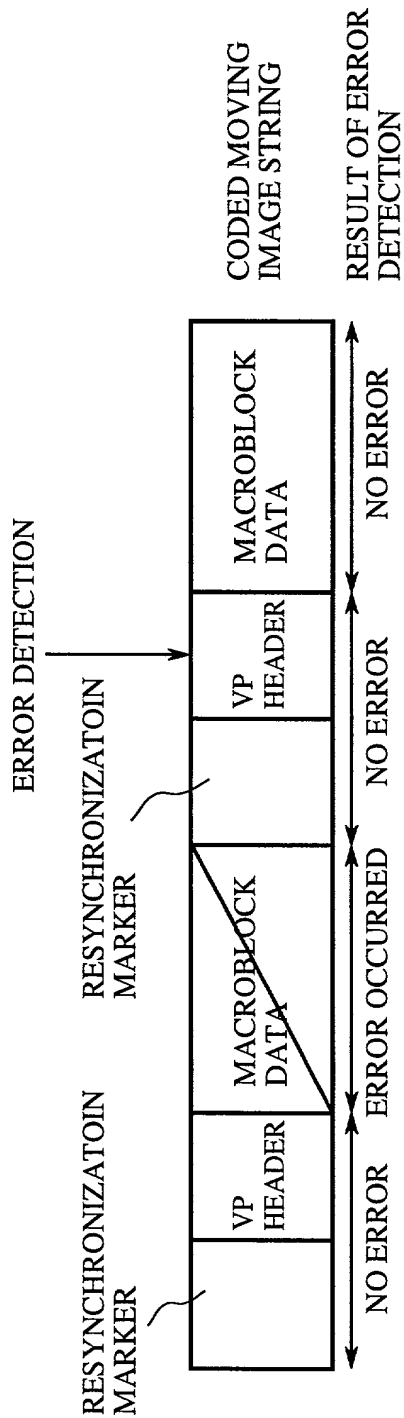


FIG.13B

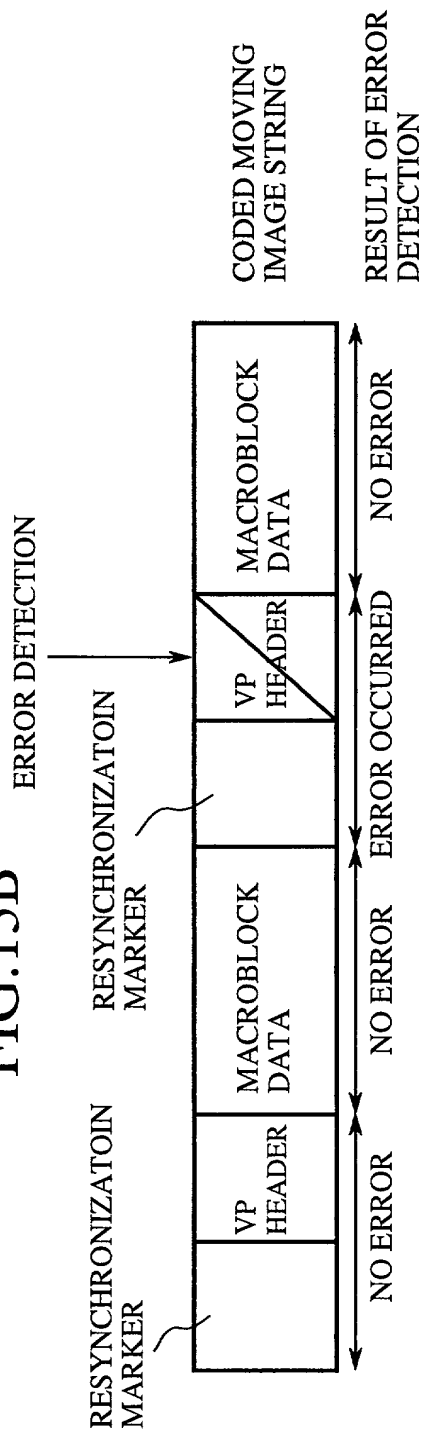




FIG.14

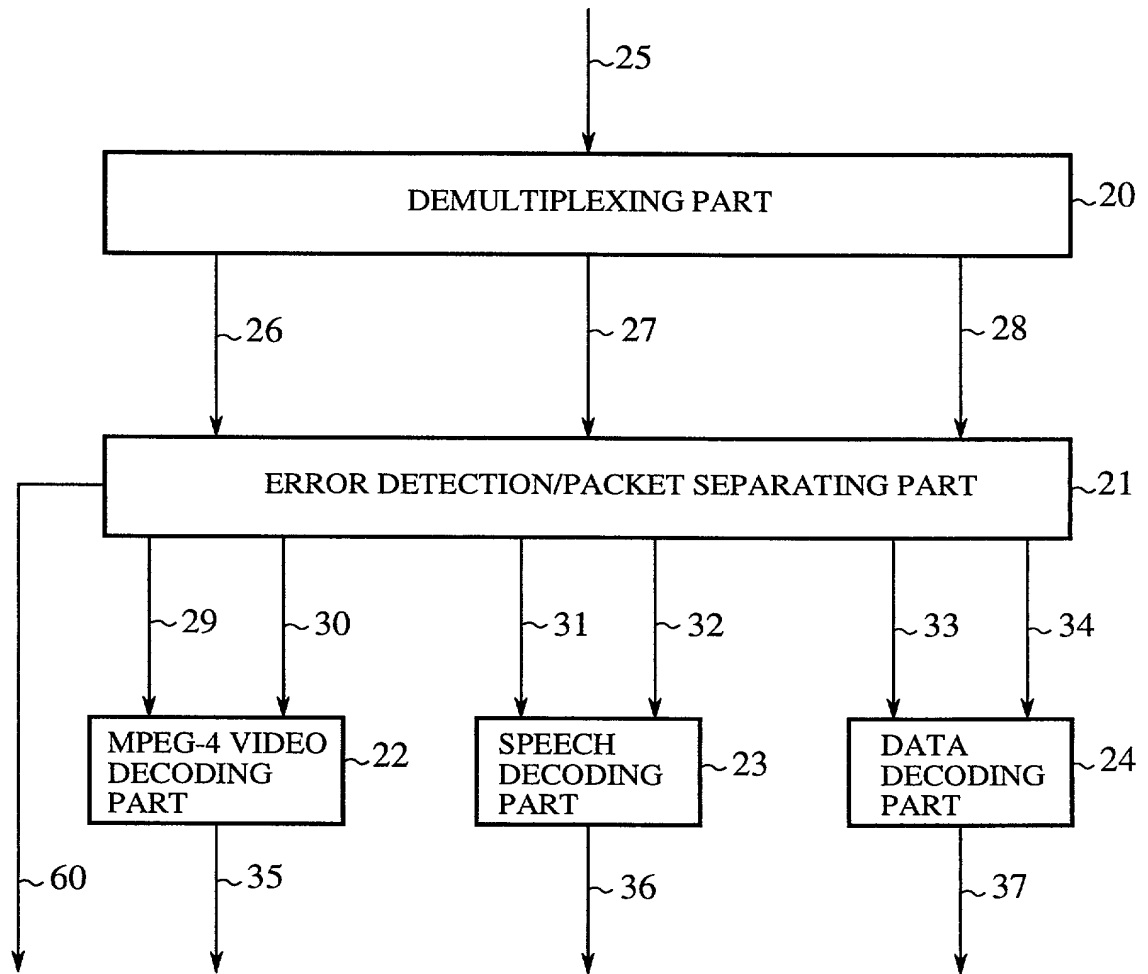


FIG.15

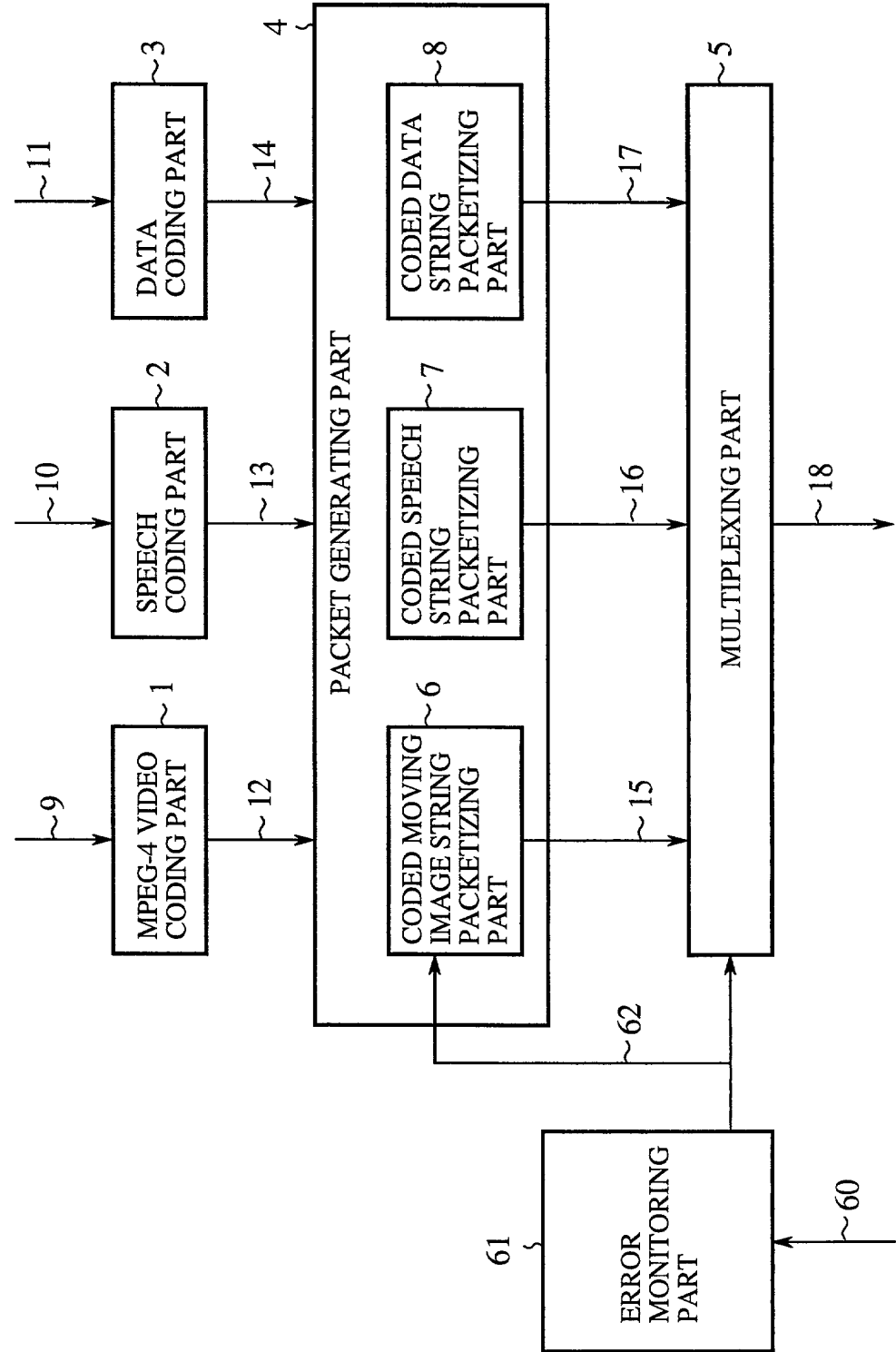


FIG.16

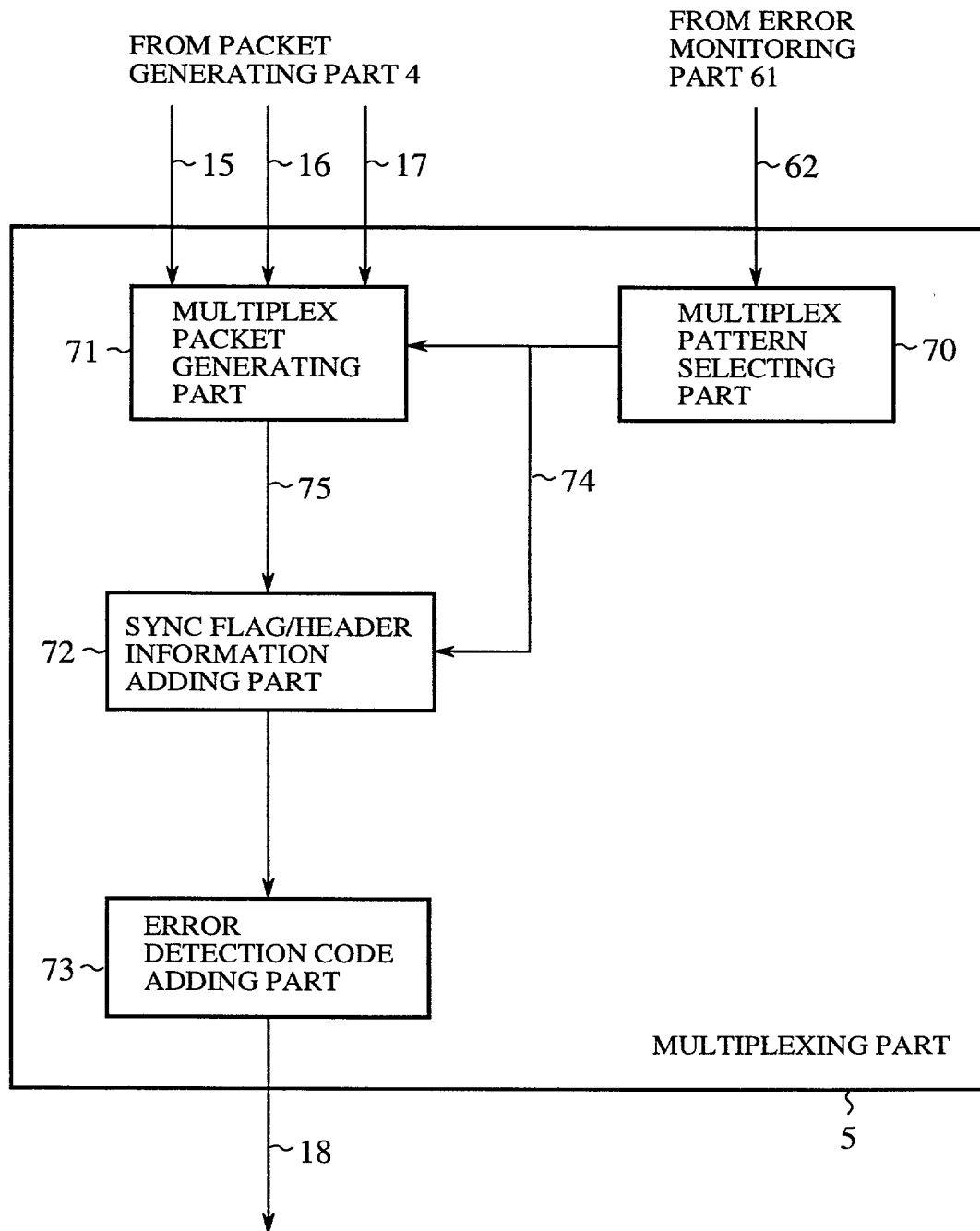


FIG.17

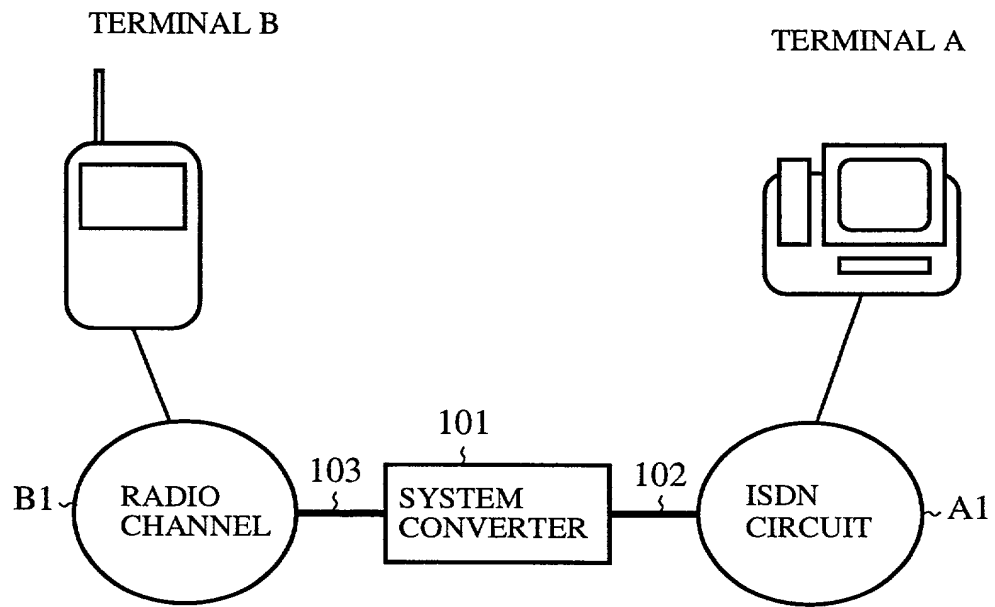


FIG.18

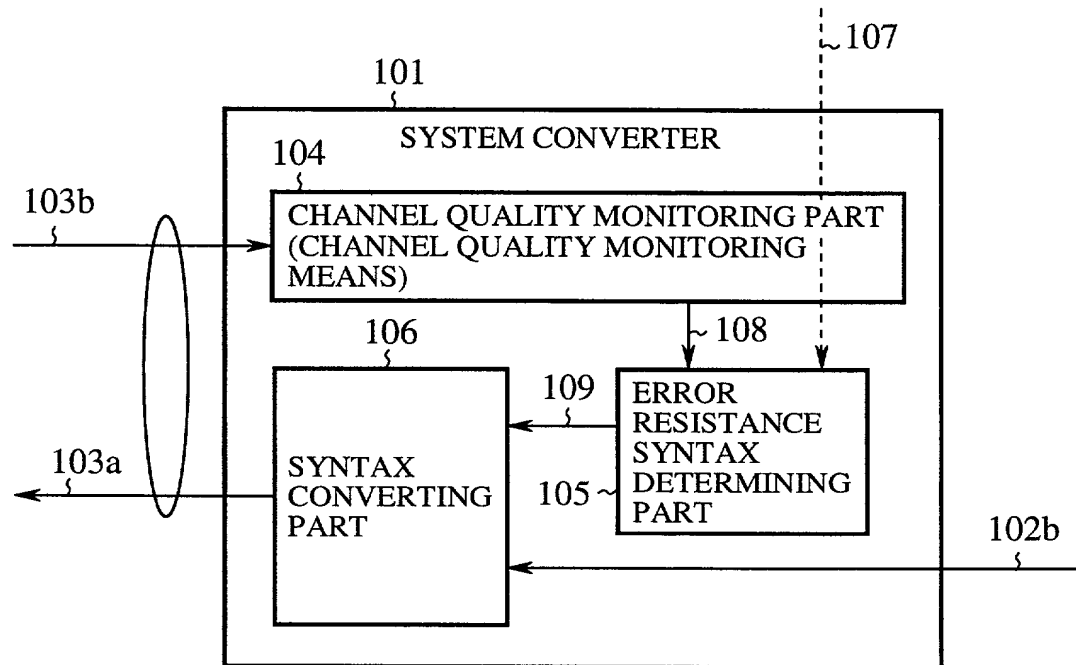


FIG.19

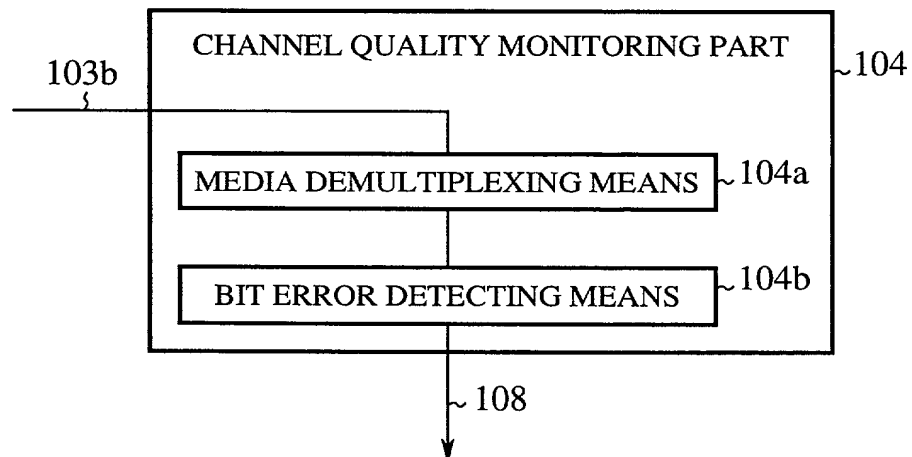


FIG.20

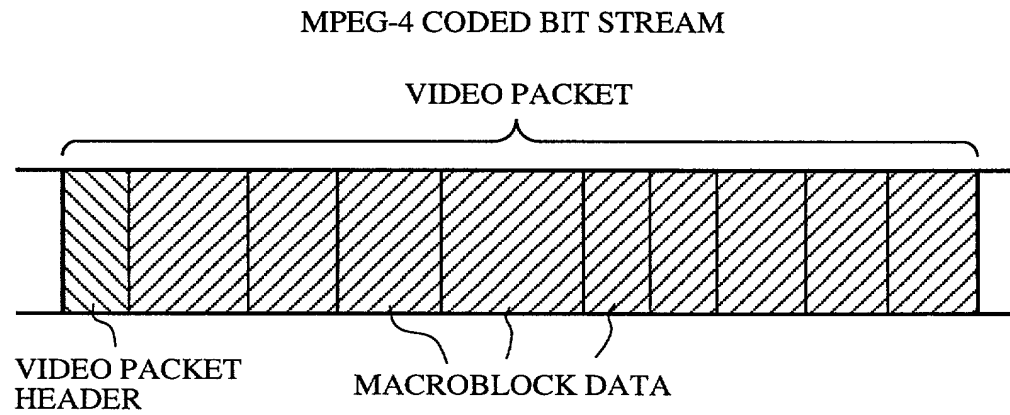


FIG.21

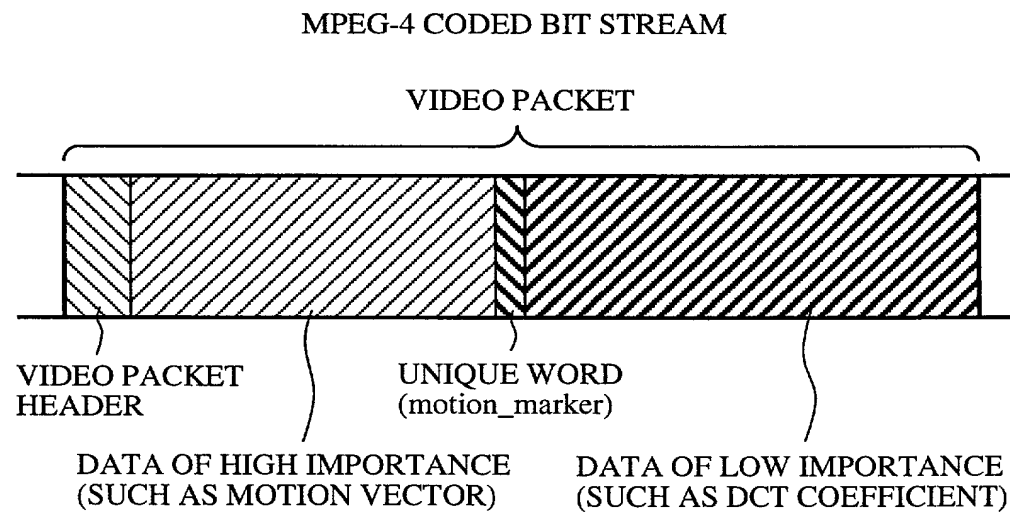


FIG.22

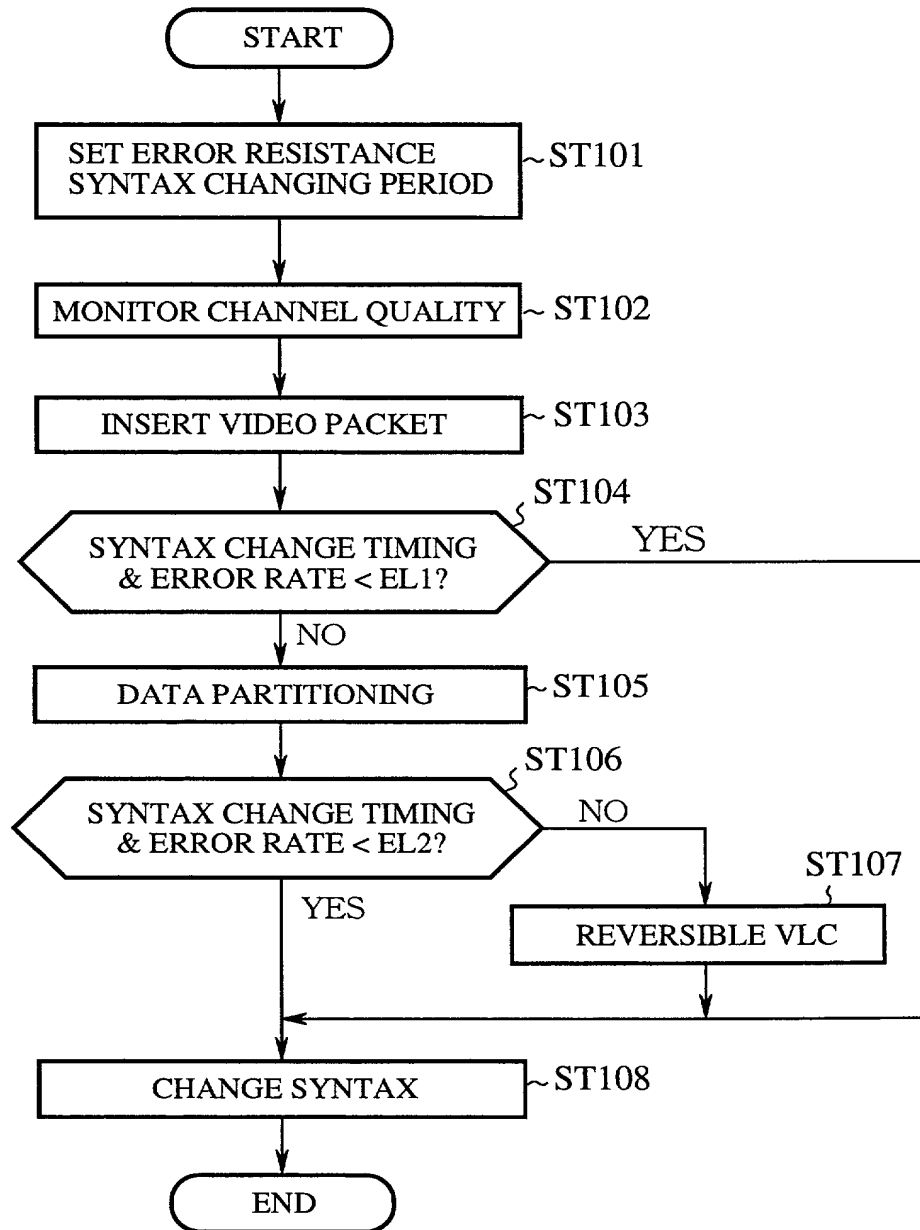


FIG.23

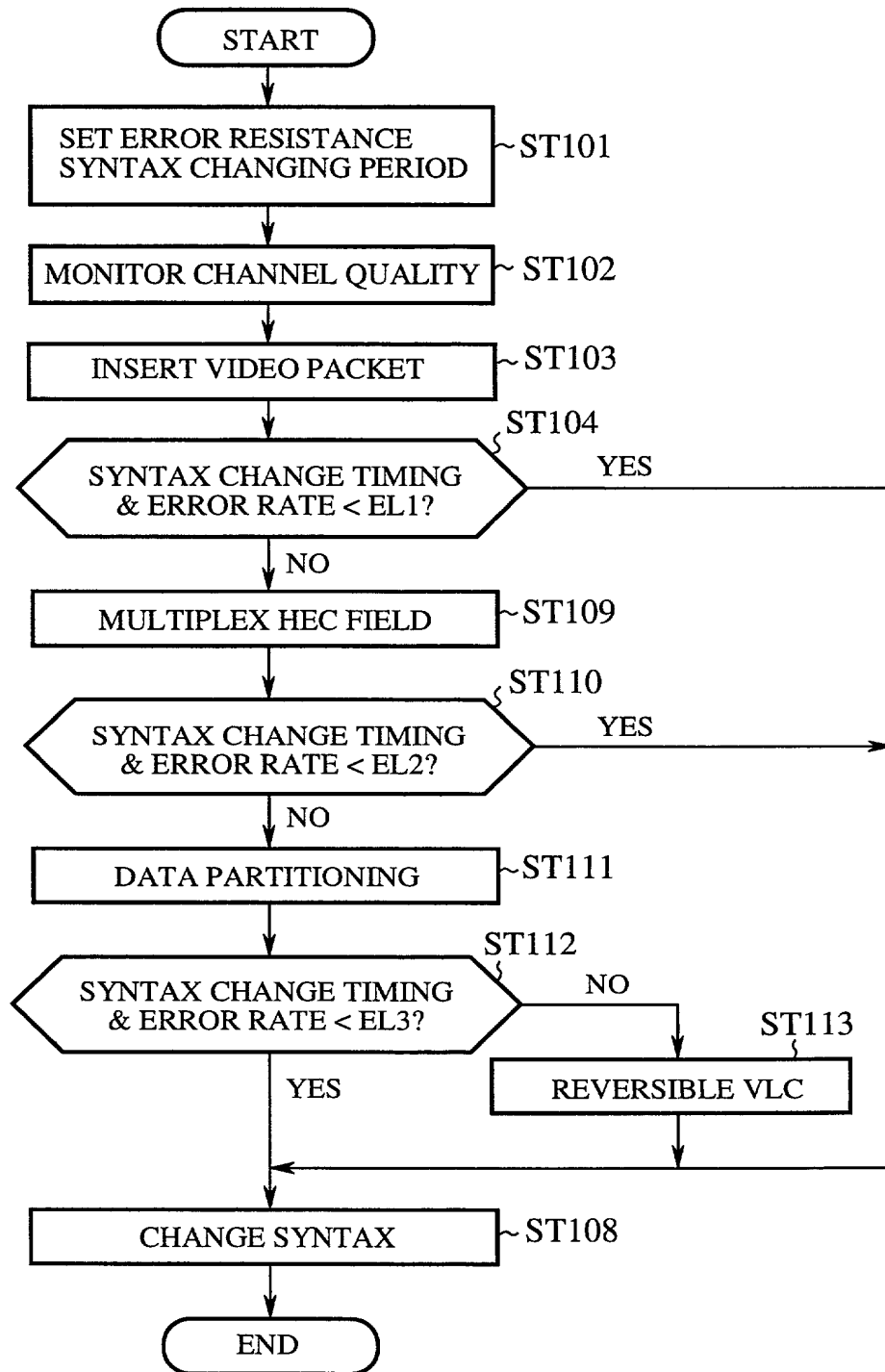




FIG.24

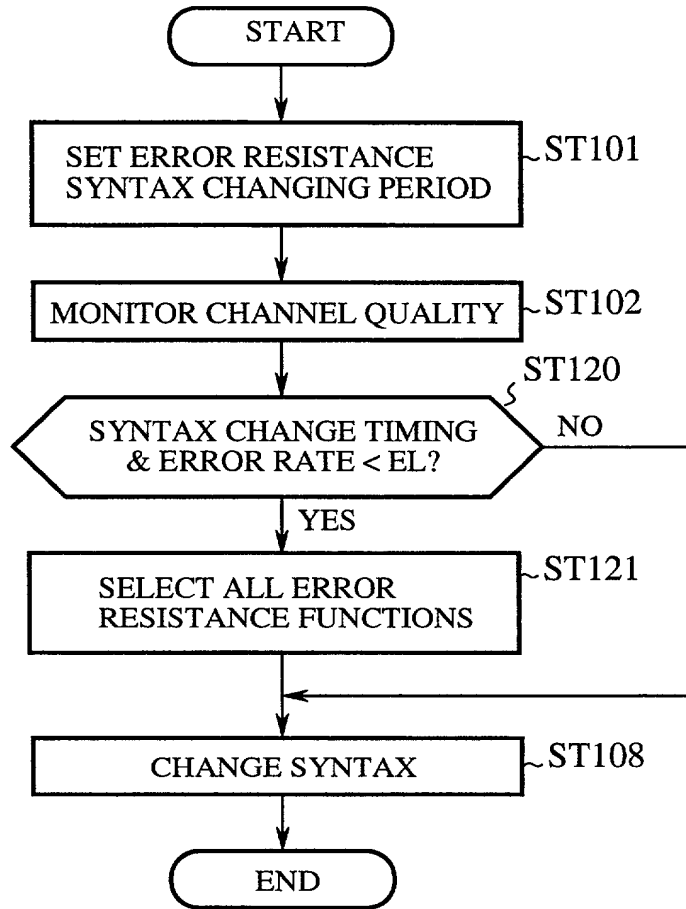


FIG.25

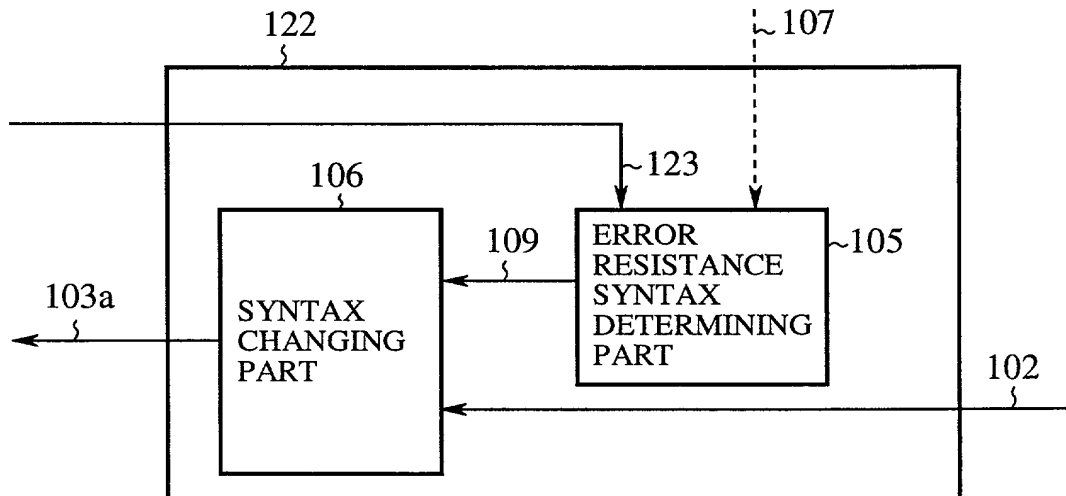


FIG.26

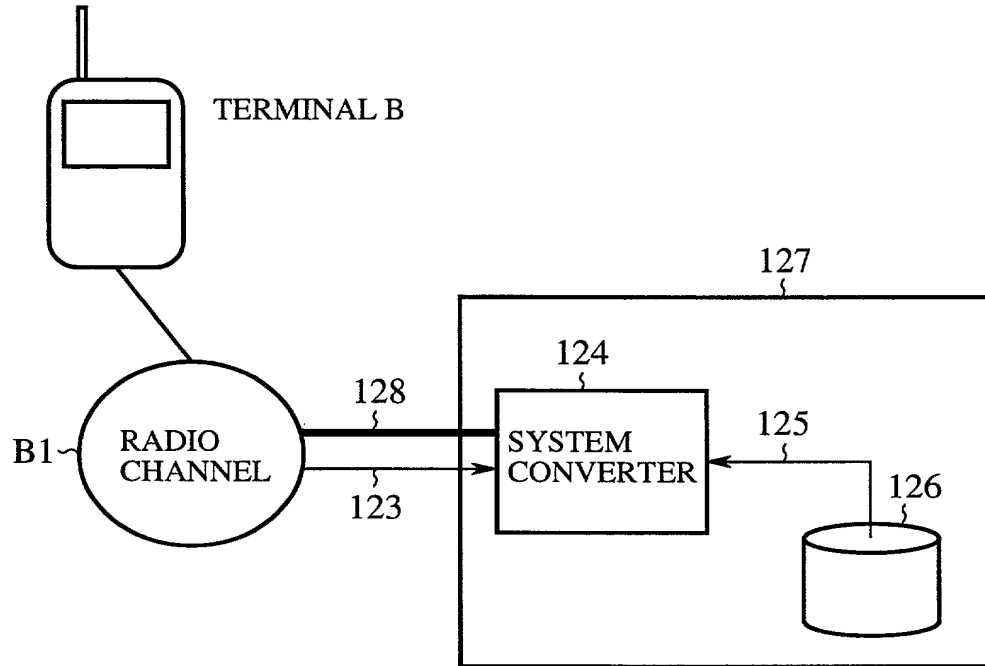


FIG.27

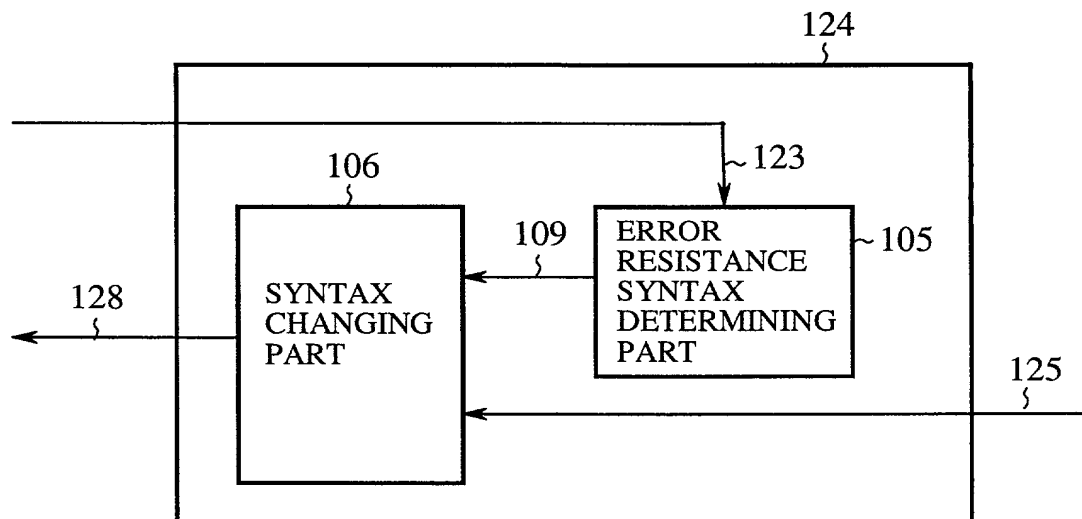


FIG.28

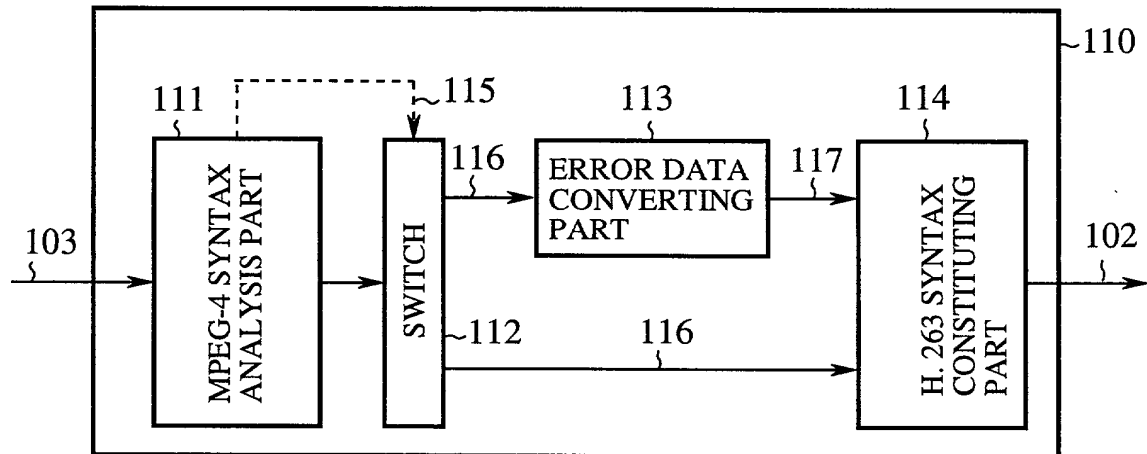


FIG.29

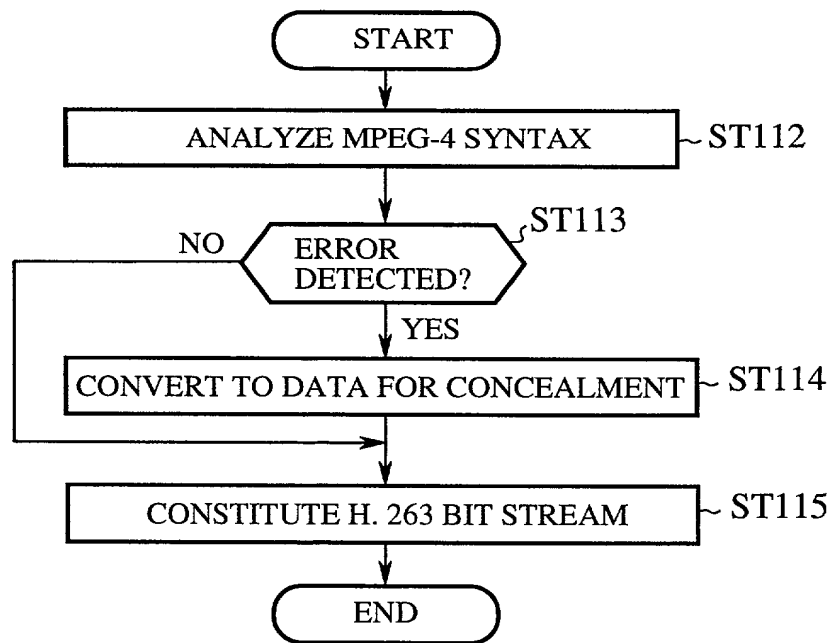


FIG.30

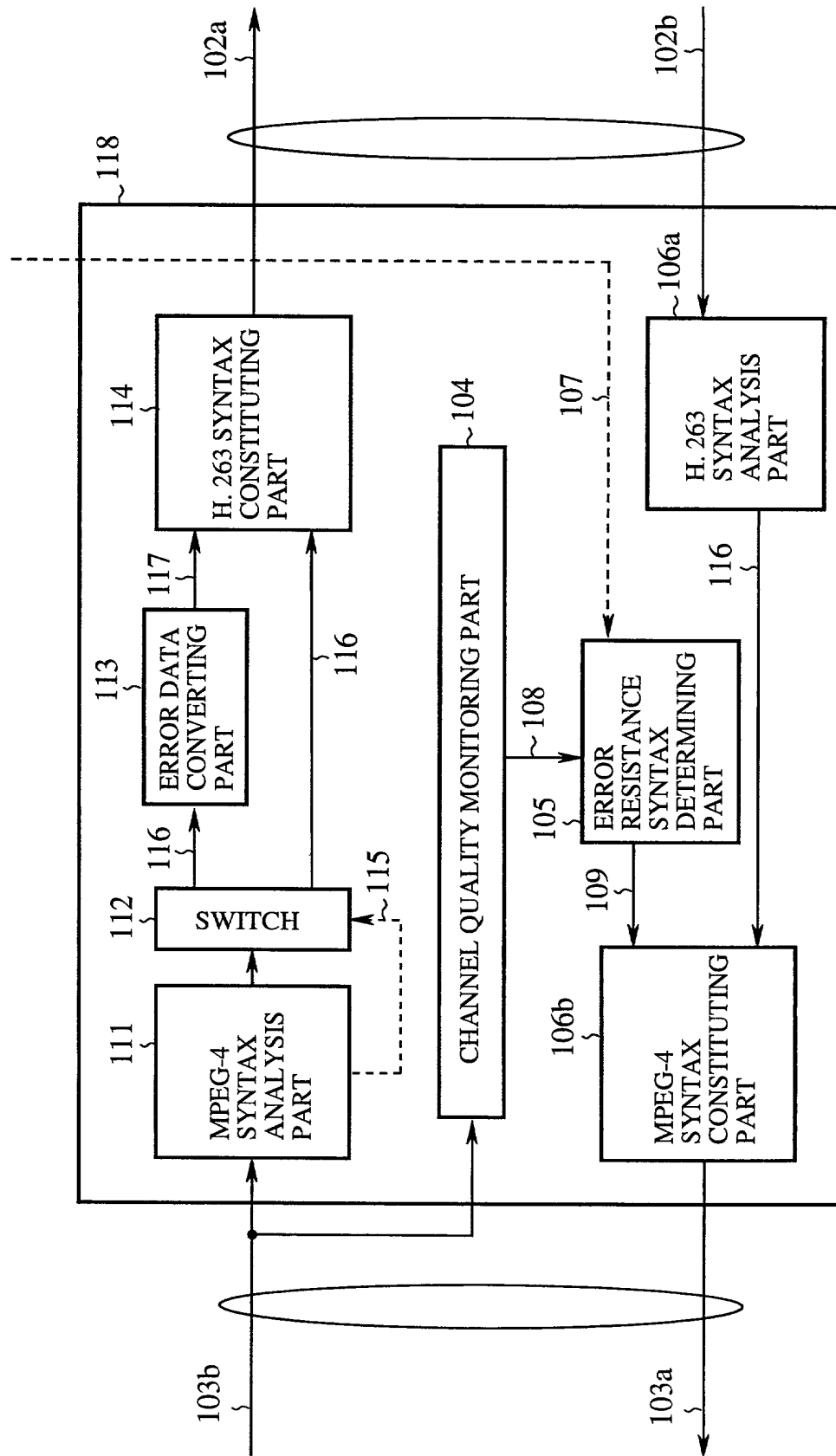


FIG.31

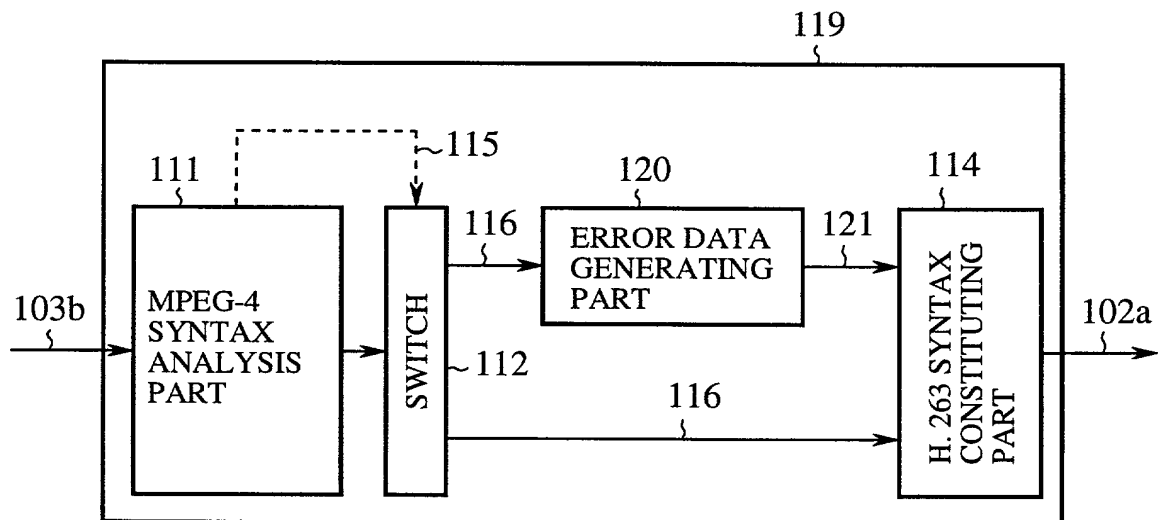
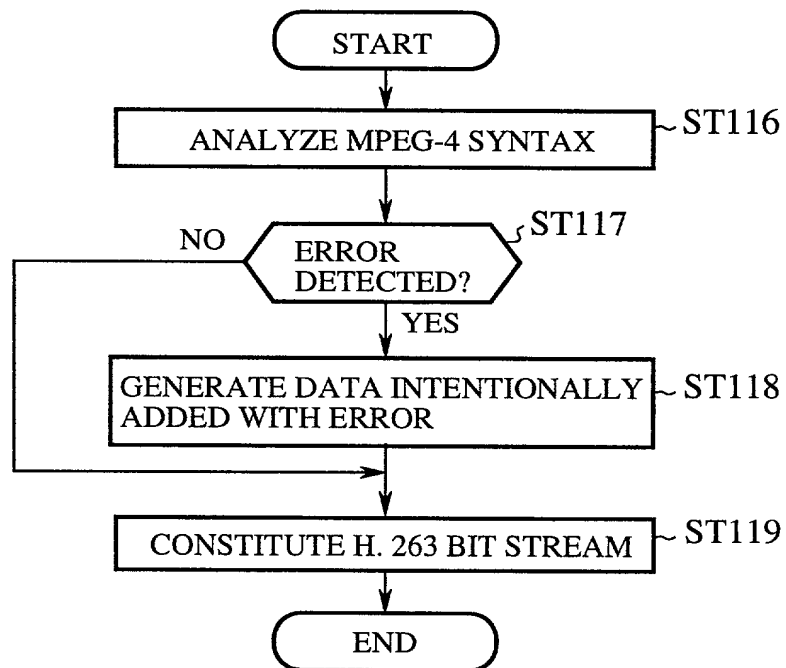


FIG.32



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**Declaration and Power of Attorney For Patent Application****特許出願宣言書及び委任状****Japanese Language Declaration****日本語宣言書**

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

"PACKET GENERATING METHOD, VIDEO DECODING METHOD, MEDIA MULTIPLEXER, MEDIA DEMULTIPLEXER, MULTIMEDIA COMMUNICATION SYSTEM AND BIT STREAM CONVERTER"

上記発明の明細書（下記の欄でx印がついていない場合は、本書に添付）は、

the specification of which is attached hereto unless the following box is checked:

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（該当する場合）\_\_\_\_に訂正されました。☐ was filed on \_\_\_\_\_  
as United States Application Number or  
PCT International Application Number  
\_\_\_\_\_ and was amended on  
\_\_\_\_\_ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されたとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

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**Japanese Language Declaration**  
(日本語宣言書)

私は、米国法典第35編119条(a)-(d)項又は365条(b)項に基づき下記の、米国外の国の少なくとも一カ国を指定している特許協力条約365(a)項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

**Prior Foreign Application(s)**

外国での先行出願

11-043707

(Number)

(番号)

11-049857

(Number)

(番号)

Japan

(Country)

(国名)

Japan

(Country)

(国名)

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Not Claimed

優先権主張なし

22/February/1999

(Day/Month/Year Filed)

(出願年月日)

26/February/1999

(Day/Month/Year Filed)

(出願年月日)

☐☐

私は、第35編米国法典119条(e)項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

(Application No.)

(出願番号)

(Filing Date)

(出願日)

(Application No.)

(出願番号)

(Filing Date)

(出願日)

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PCT/JP00/01009

(Application No.)

(出願番号)

22/Feb/2000

(Filing Date)

(出願日)

(Application No.)

(出願番号)

(Filing Date)

(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

Pending

(Status: Patented, Pending, Abandoned)

(現況: 特許許可済、係属中、放棄済)

(Status: Patented, Pending, Abandoned)

(現況: 特許許可済、係属中、放棄済)

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## Japanese Language Declaration (日本語宣言書)

委任状： 私は下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁理士または代理人として、下記の者を指名いたします。(弁理士、または代理人の氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)

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発明者の署名	日付	Inventor's signature	Date
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国籍	Citizenship		
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(第三以降の共同発明者についても同様に記載し、署名をすること)

(Supply similar information and signature for third and subsequent joint inventors.)



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国籍		Citizenship	
郵便の宛先		Post Office Address	
第8の共同発明者の氏名 (該当する場合)		Full name of eighth joint inventor, if any,	
同第8発明者の署名	日付	Eighth Inventor's signature	Date
住所		Residence	
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